

FIG. 1

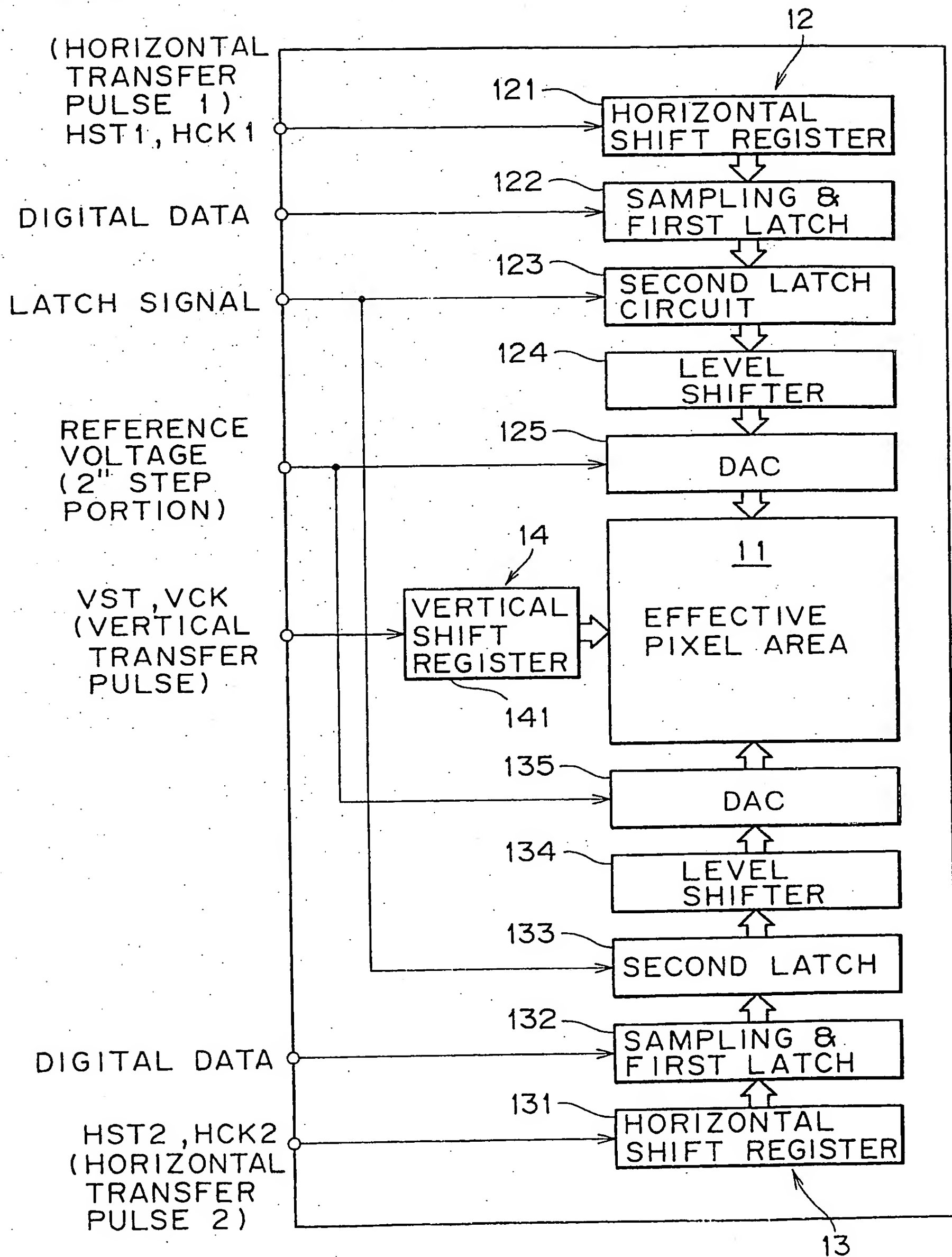


FIG. 2

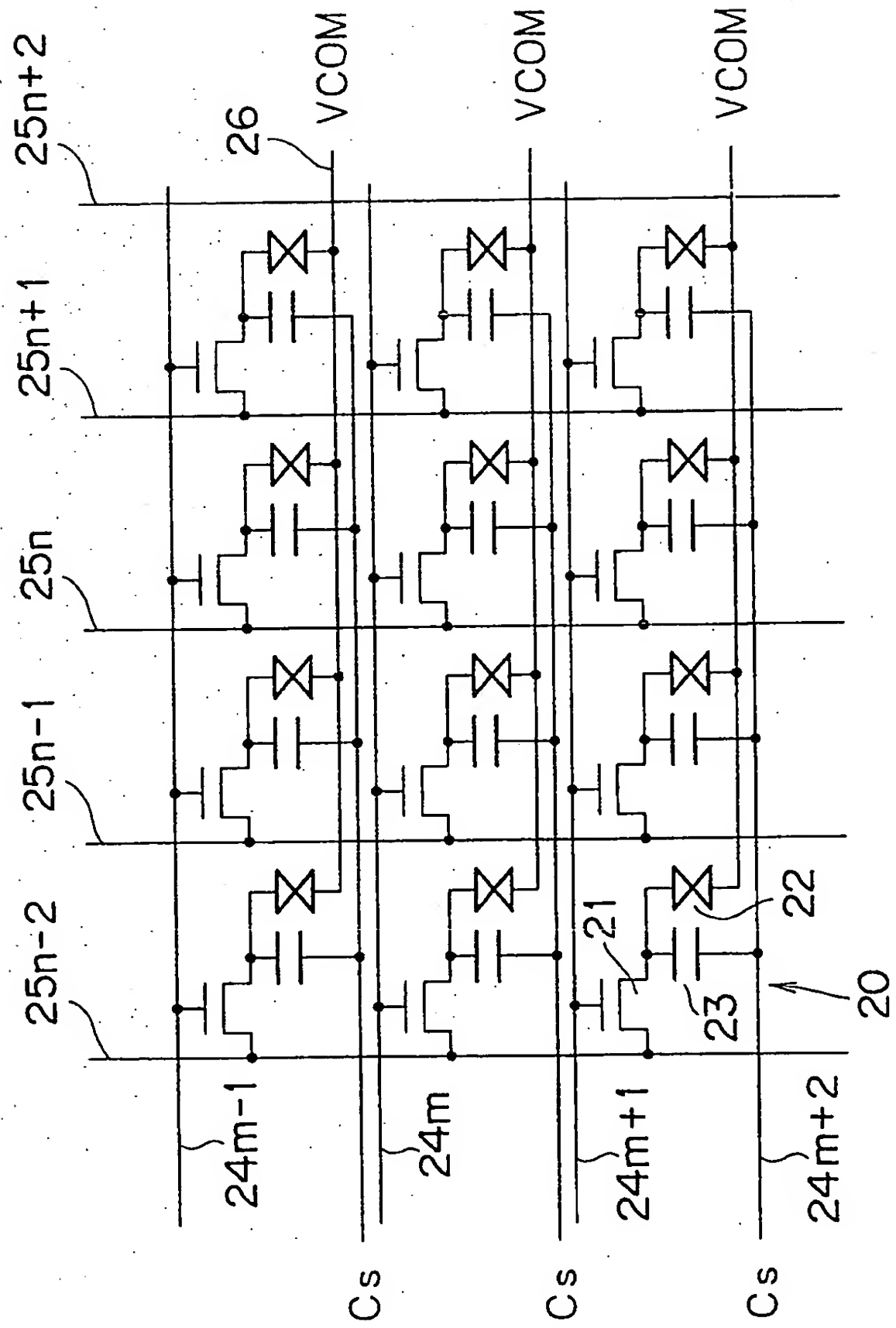
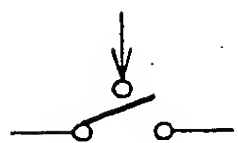
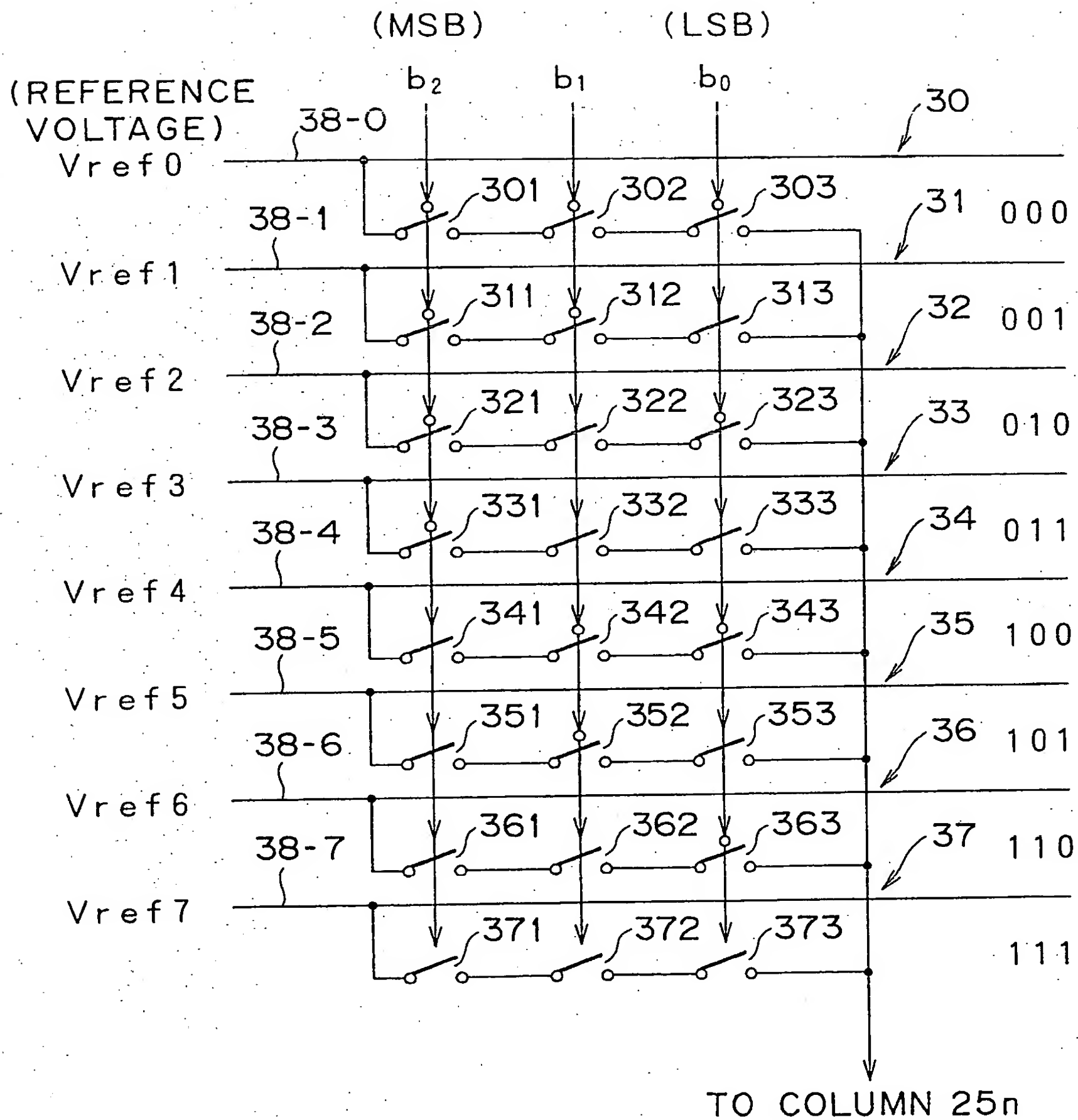
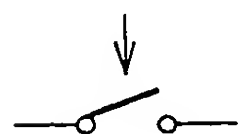


FIG. 3



NEGATIVE POLARITY
ANALOG SWITCH



POSITIVE POLARITY
ANALOG SWITCH

FIG. 4

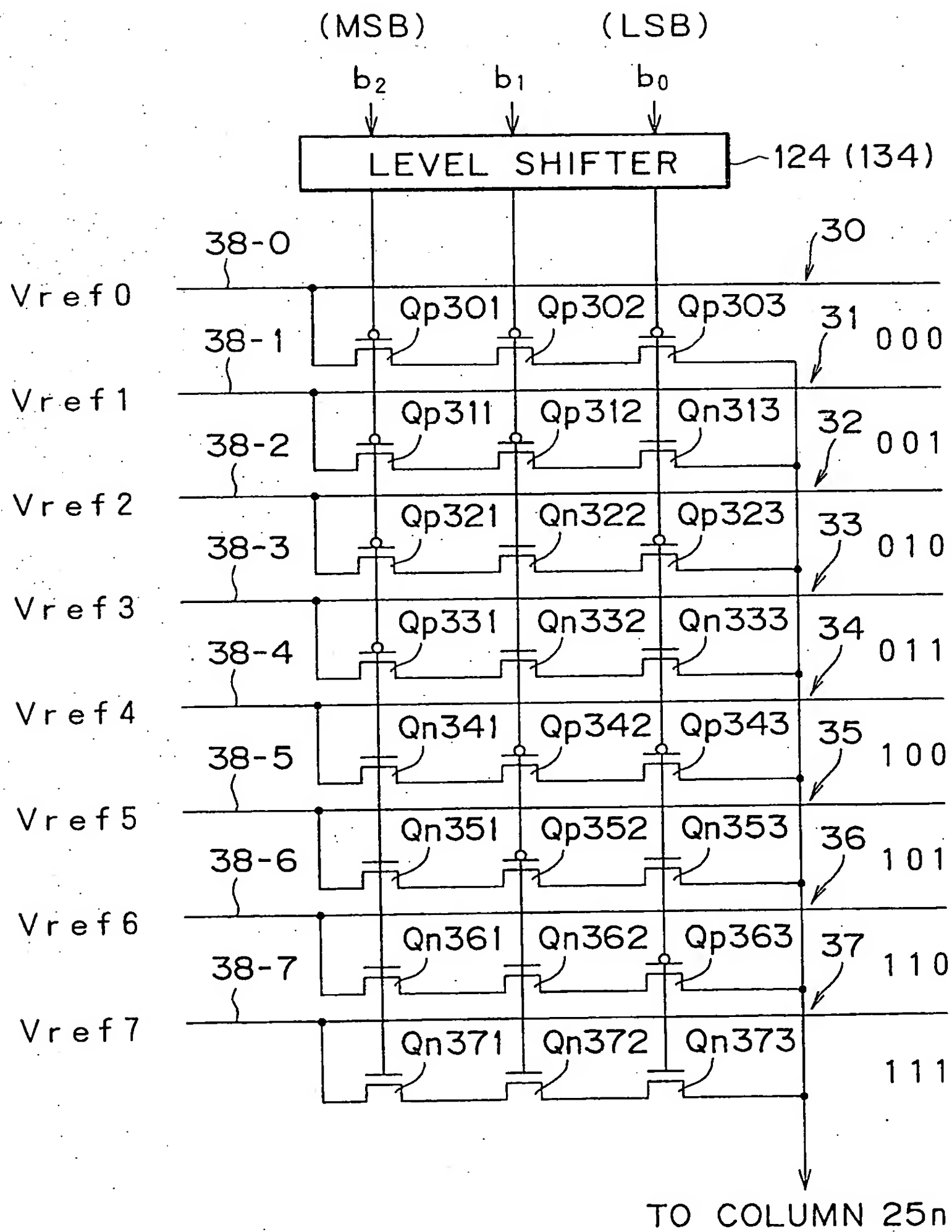


FIG. 5

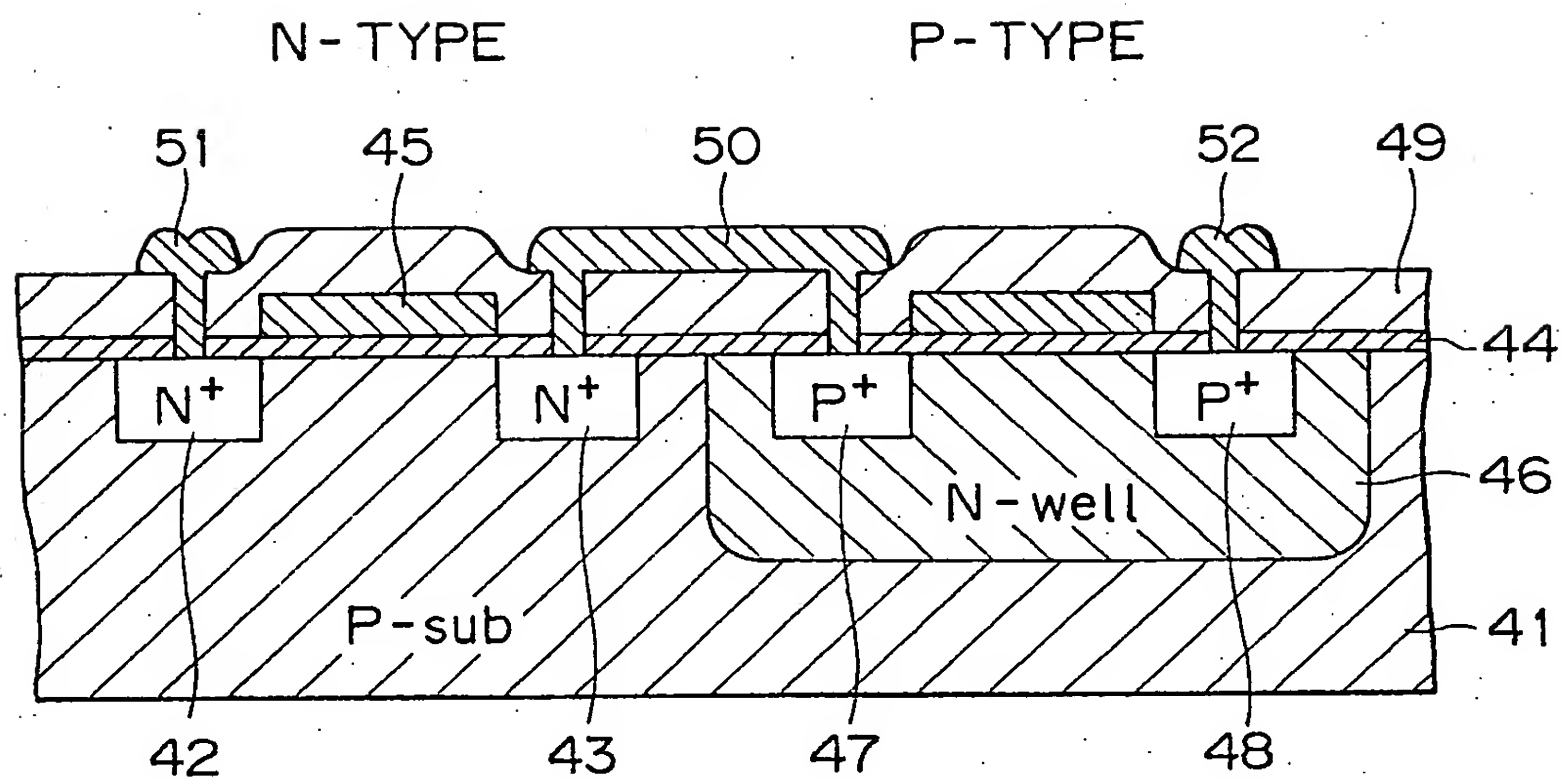


FIG. 6

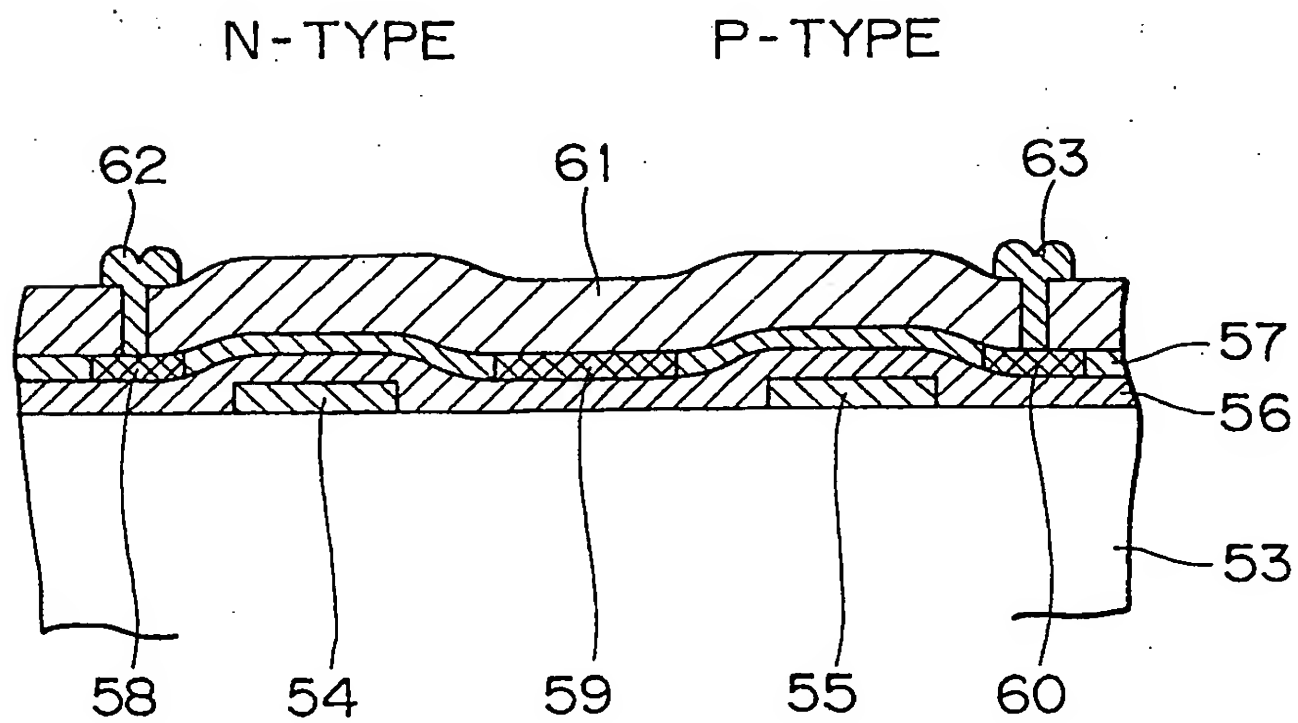


FIG. 7

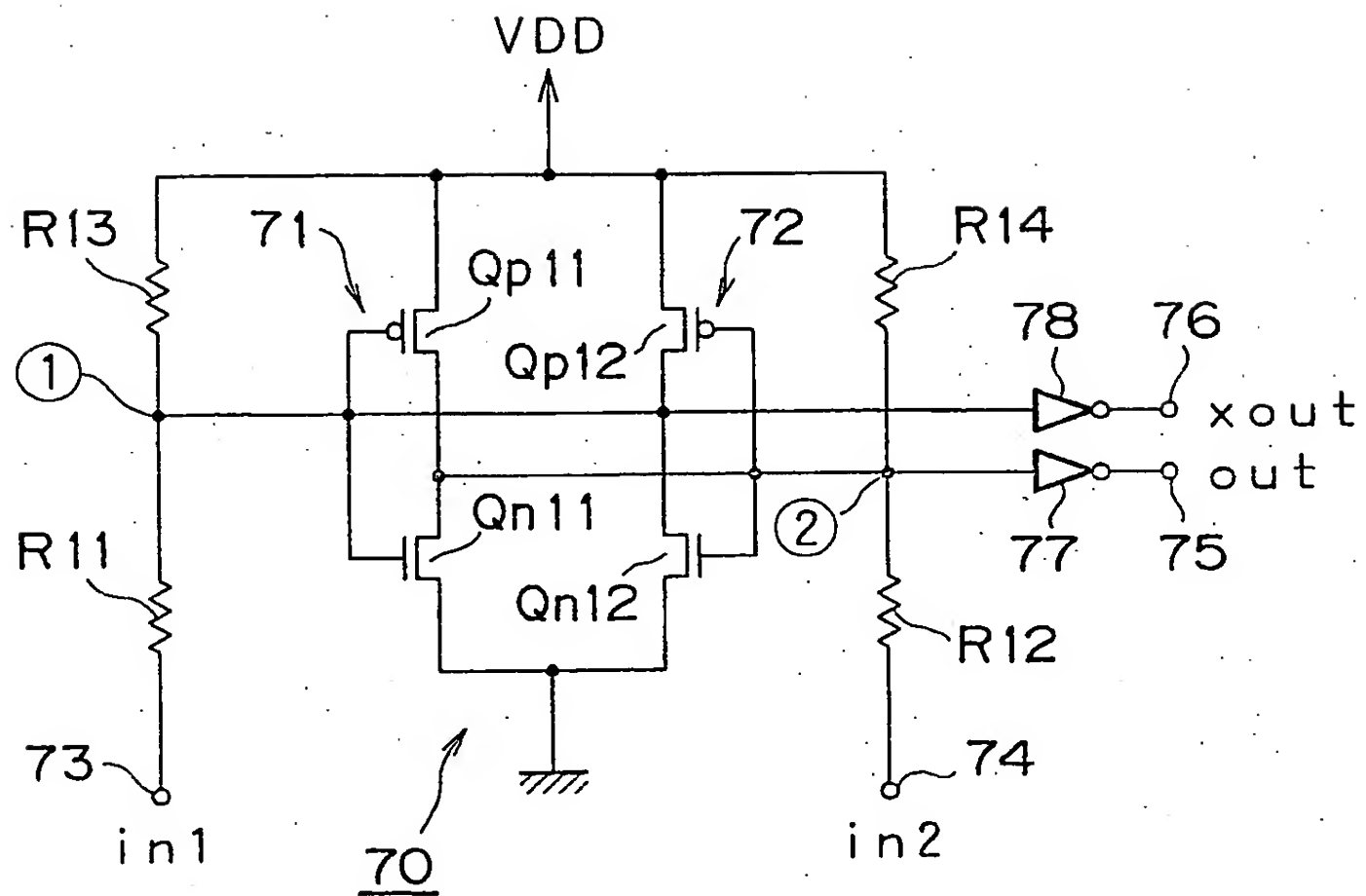


FIG. 8

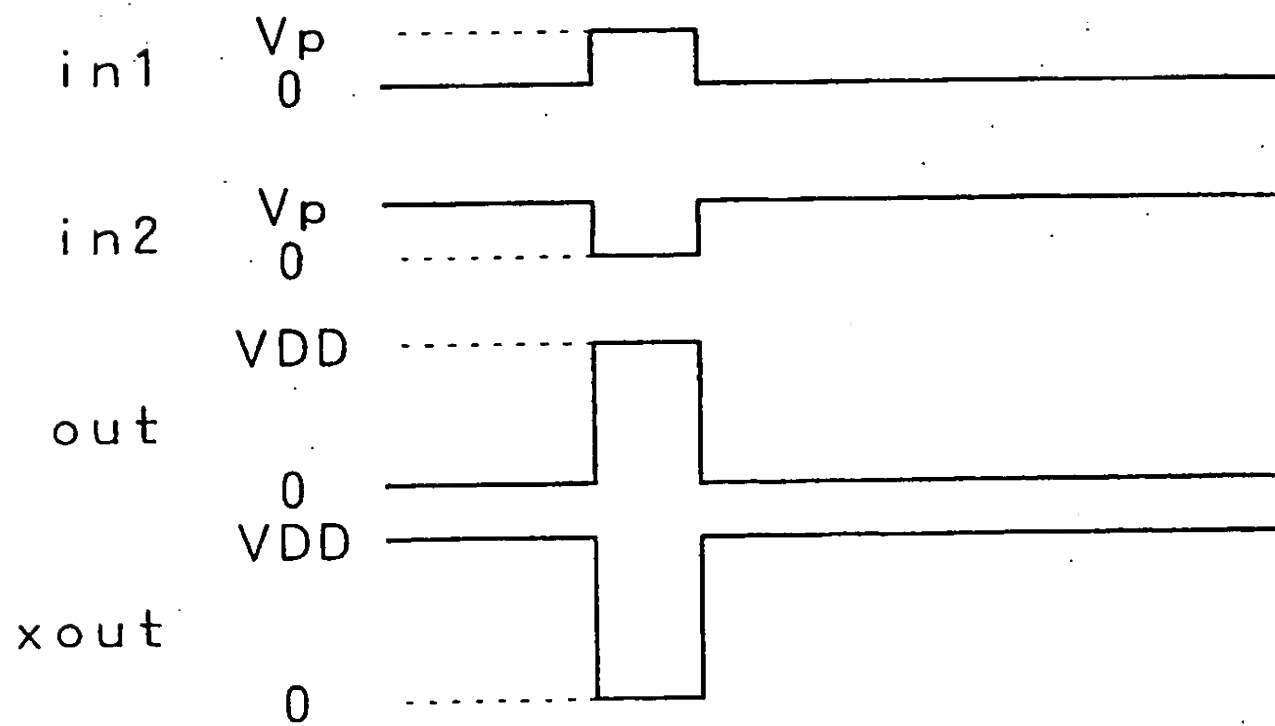


FIG. 9

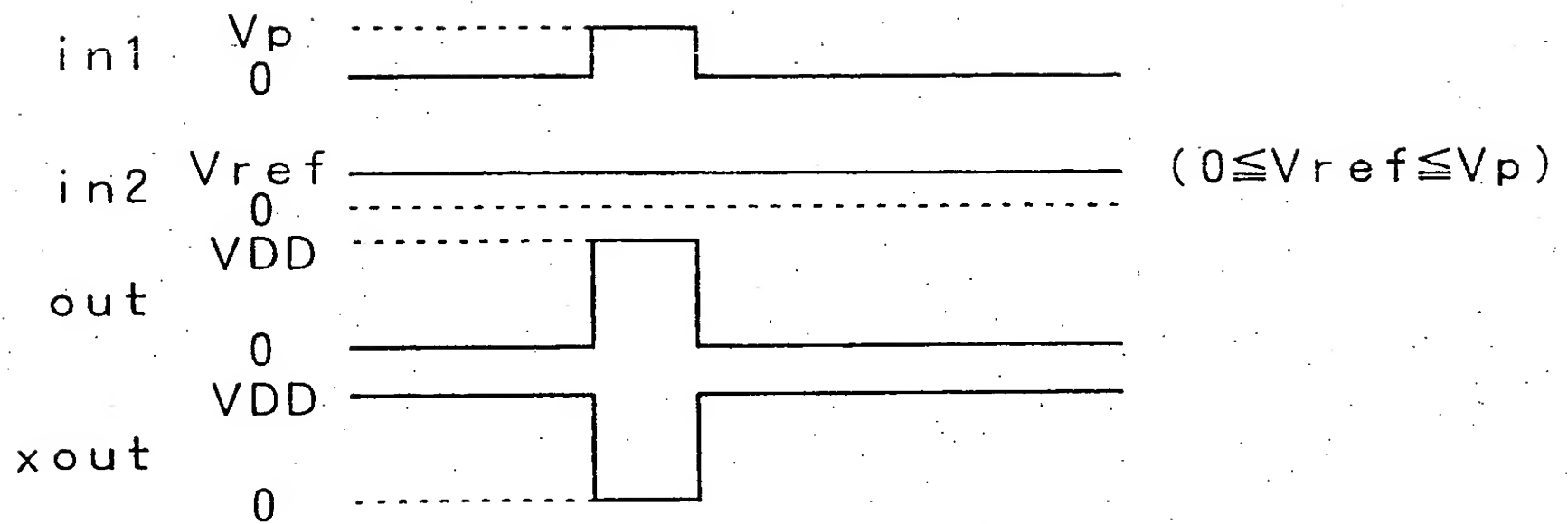


FIG. 10

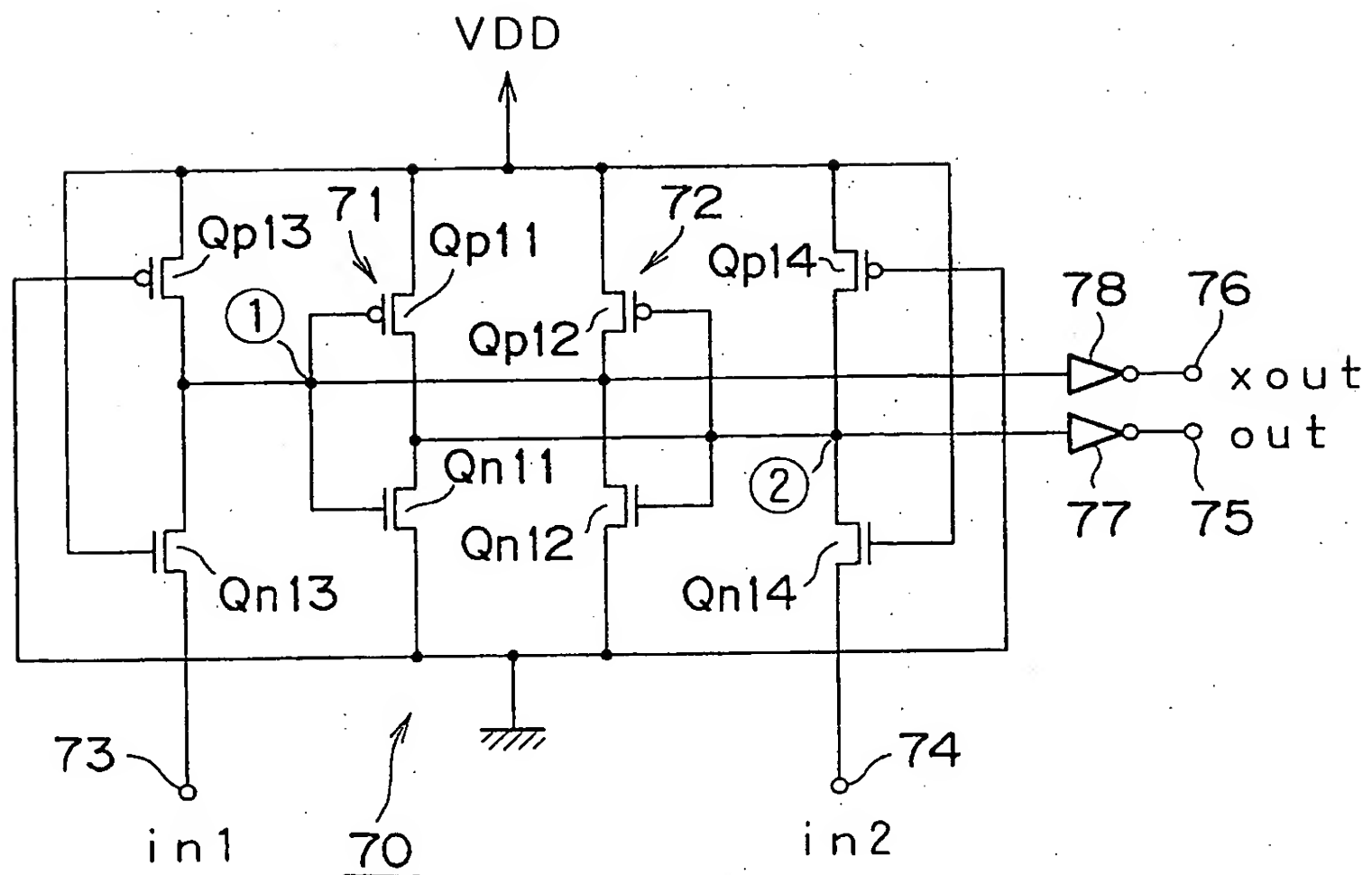


FIG. 10B RELATED ART

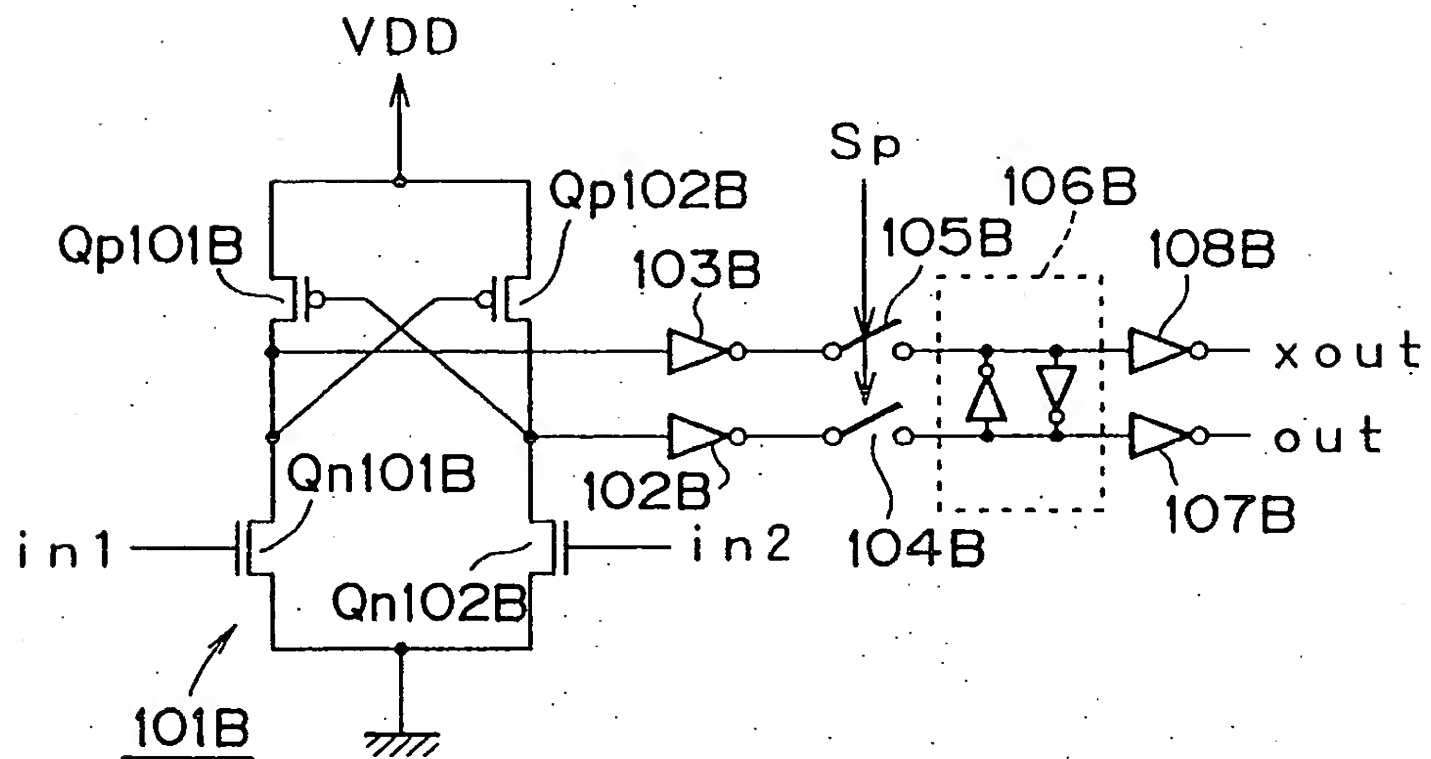
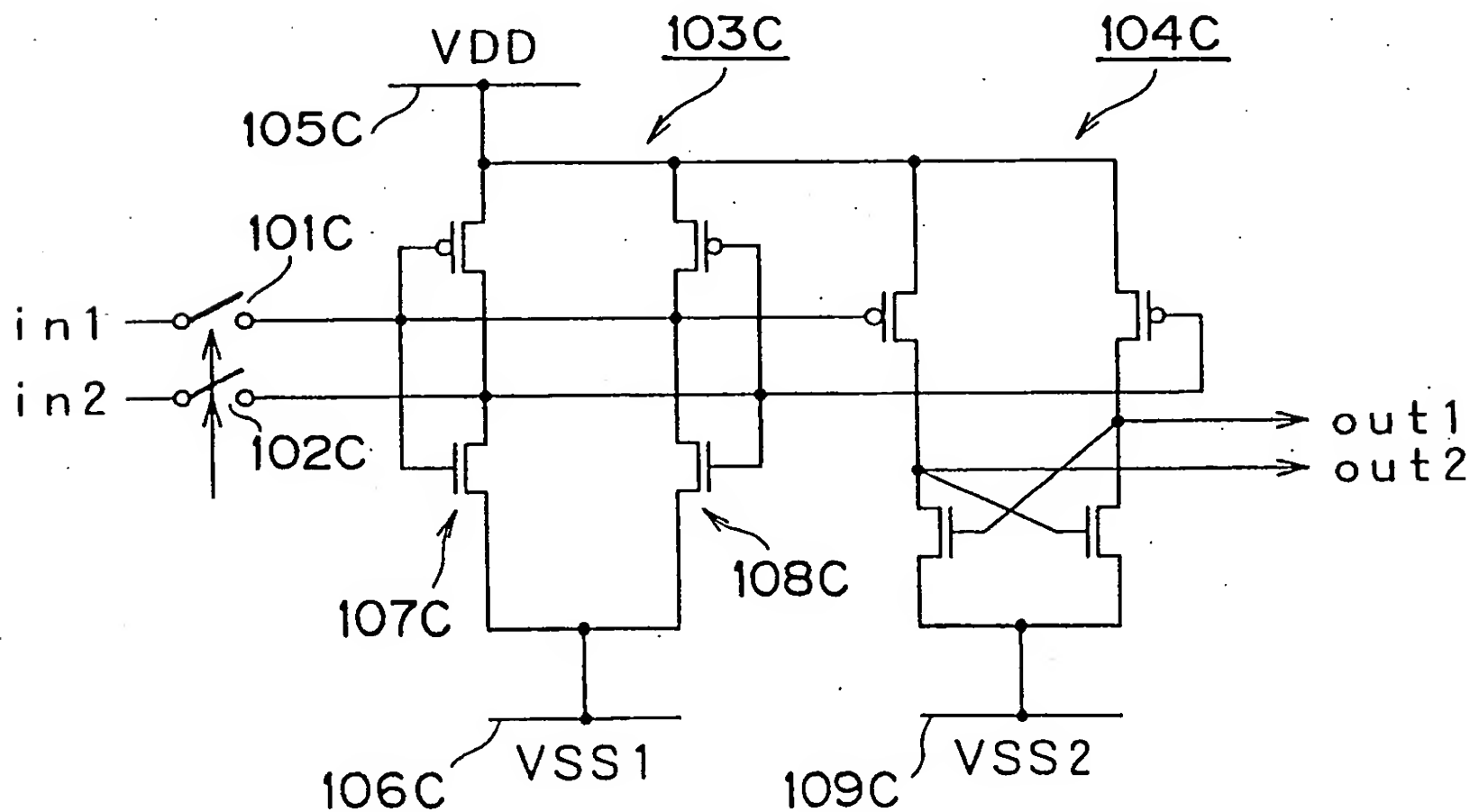


FIG. 10C RELATED ART 1



VDD



Eq

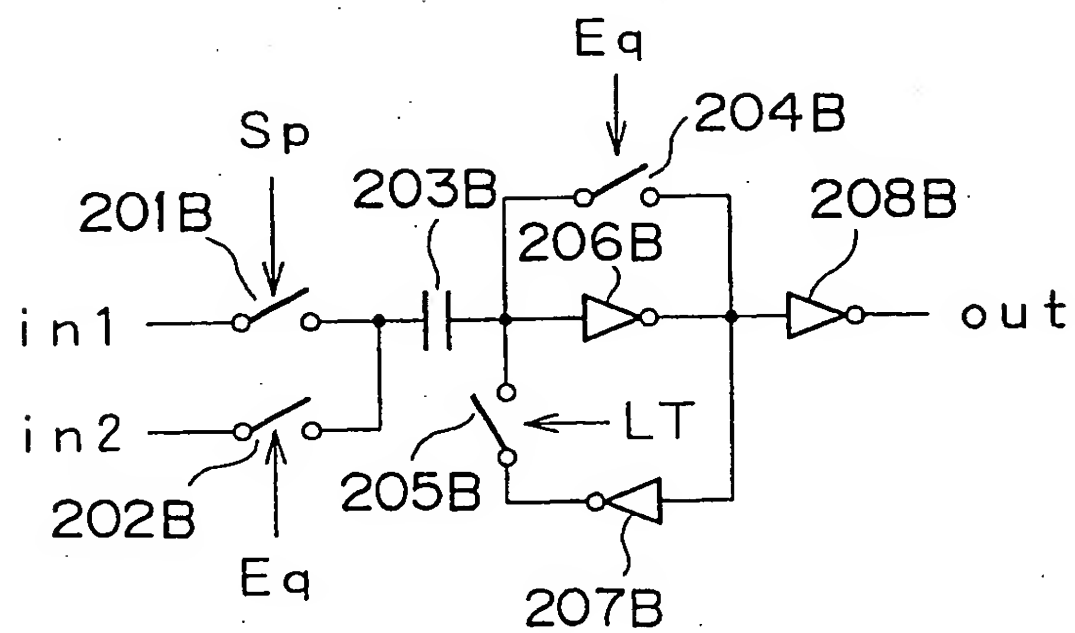


FIG. 11C
RELATED ART 2

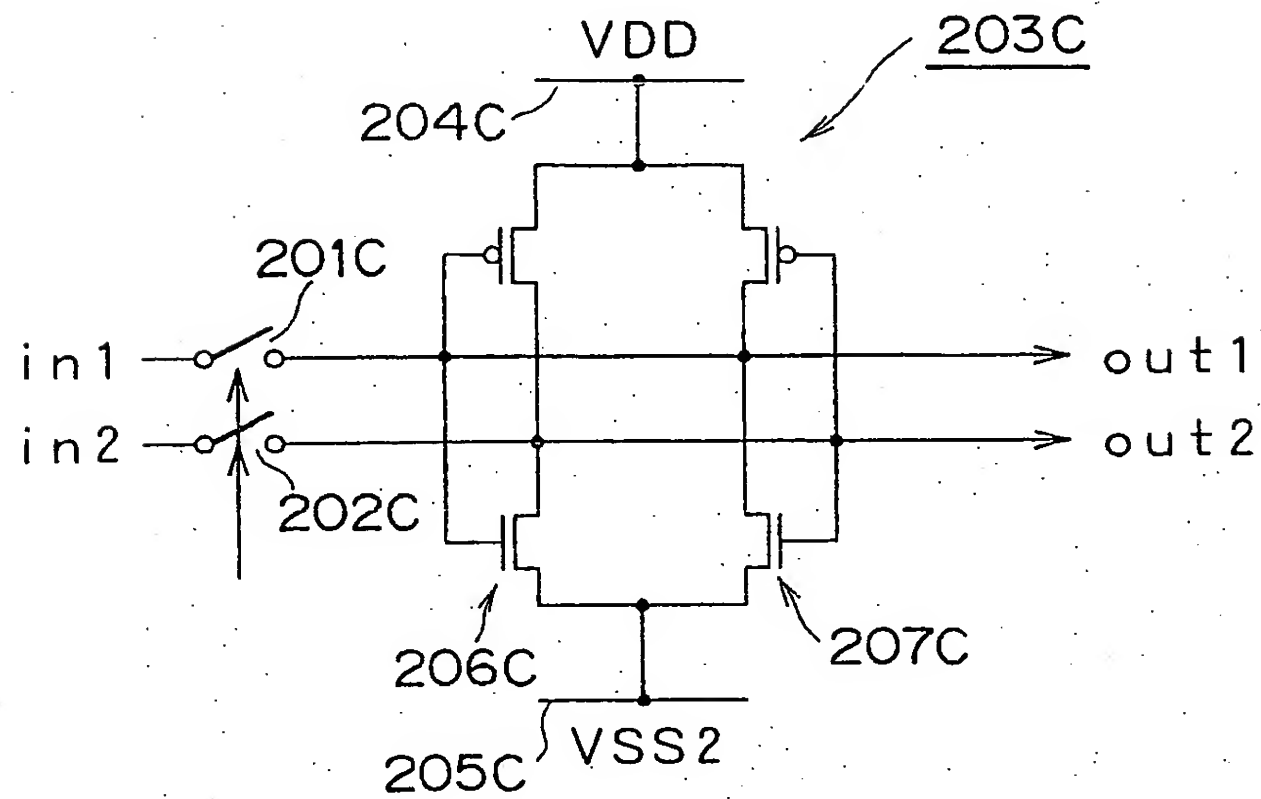


FIG. 12

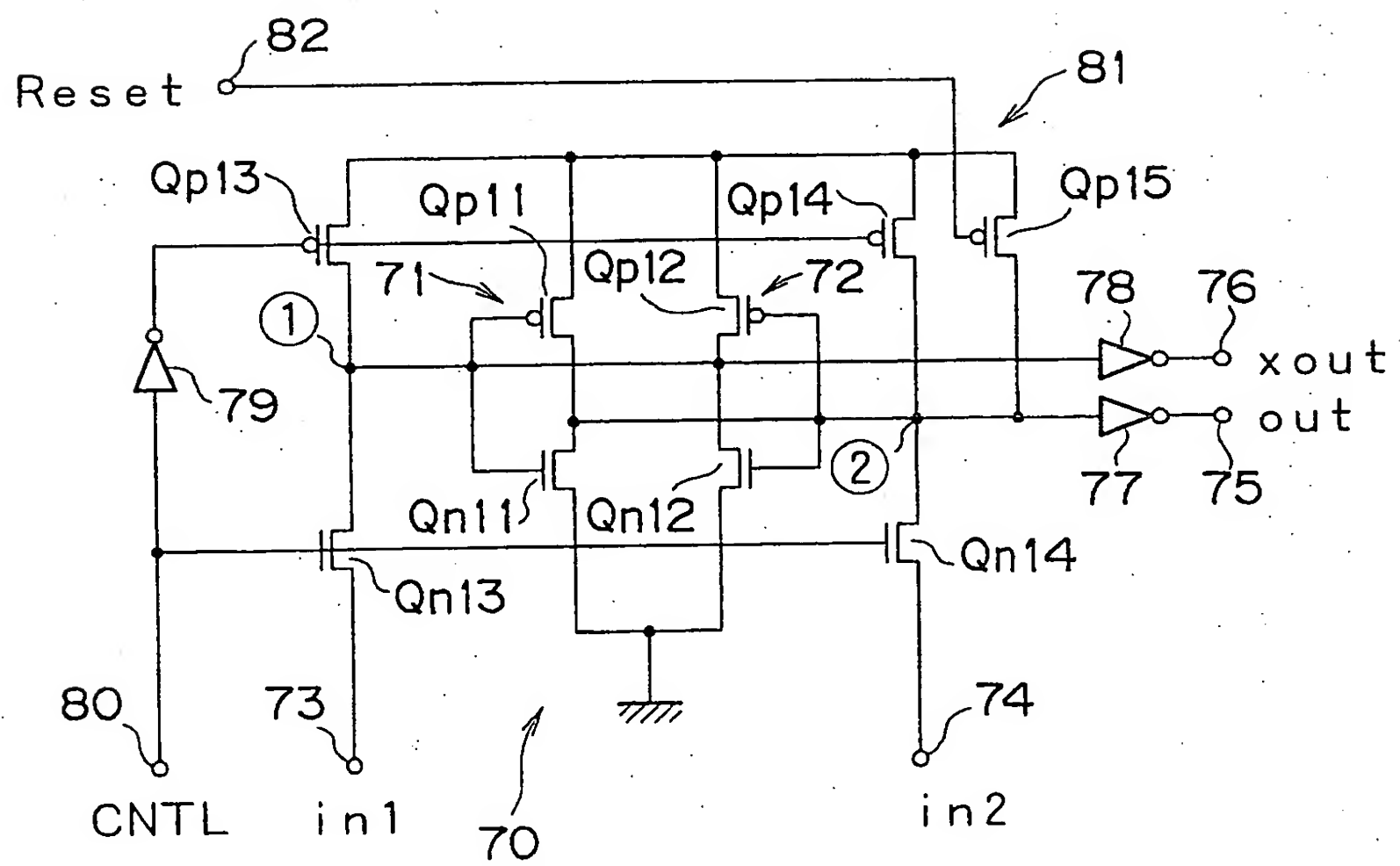


FIG. 12C

RELATED ART 3

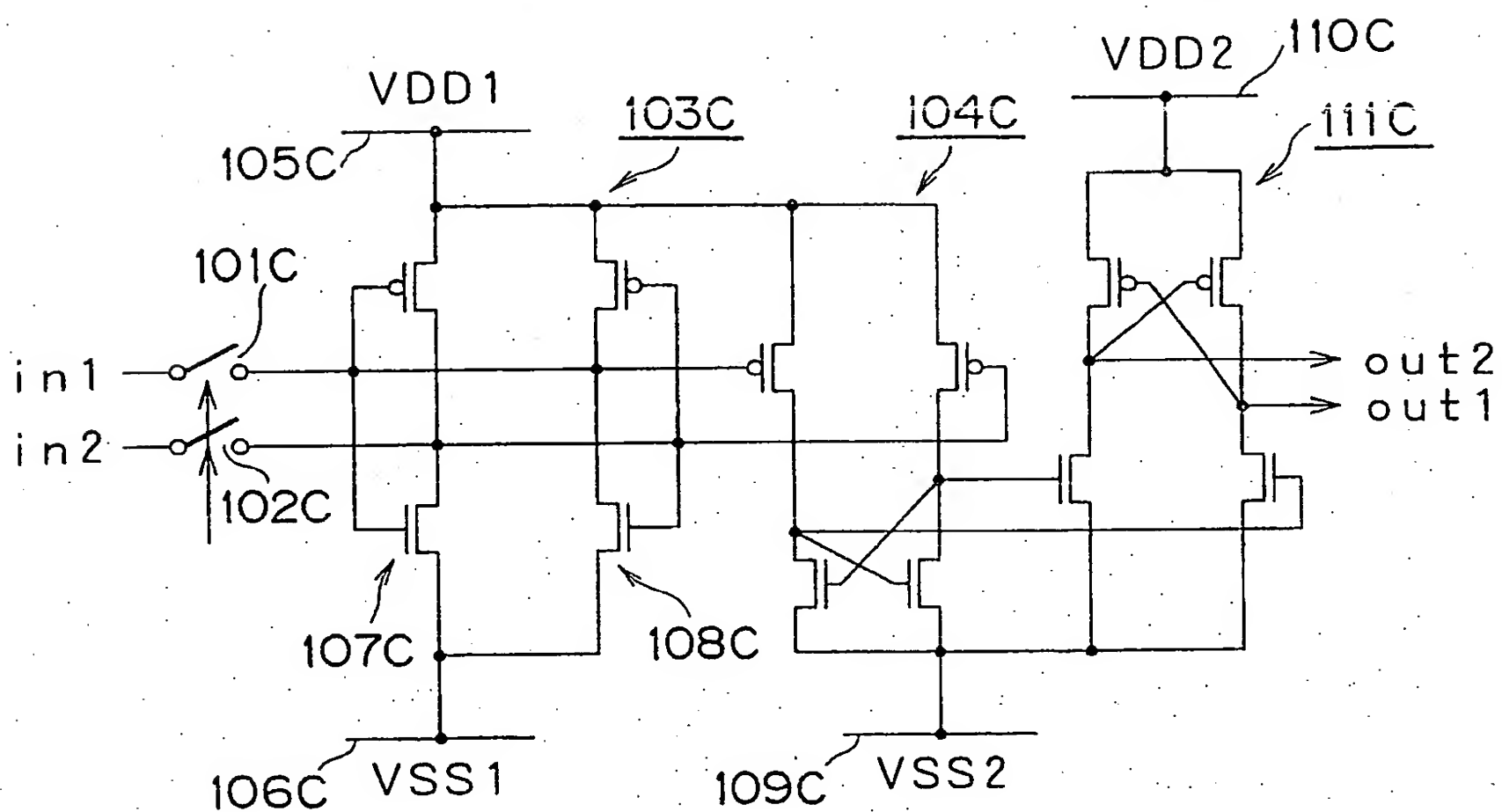


FIG. 13

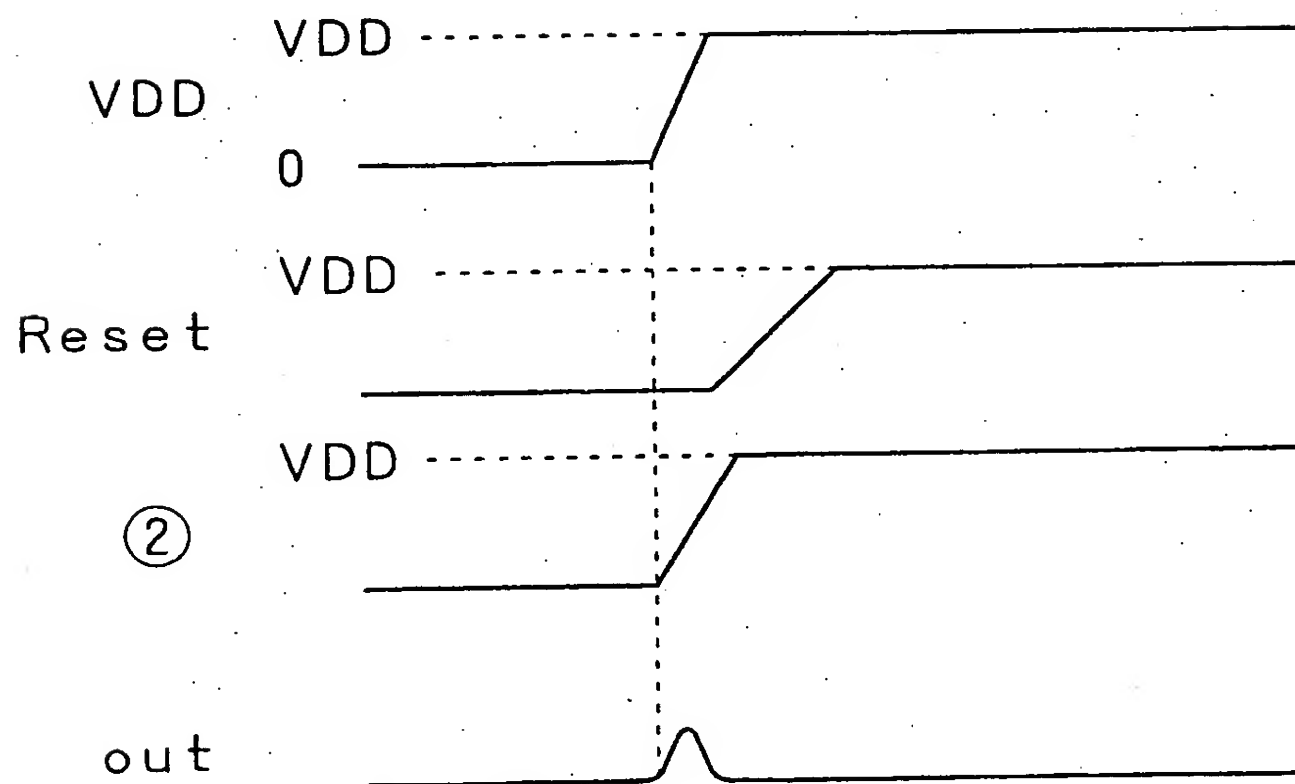


FIG. 13A
RELATED ART 1

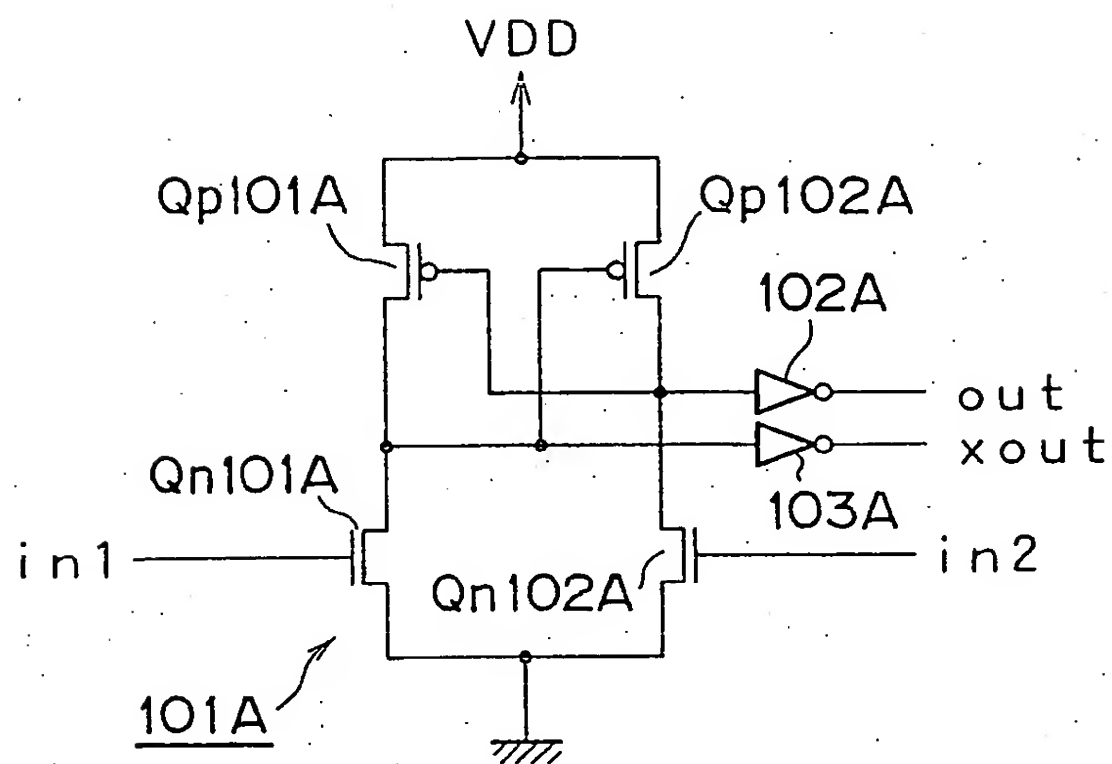


FIG. 13C
RELATED ART 4

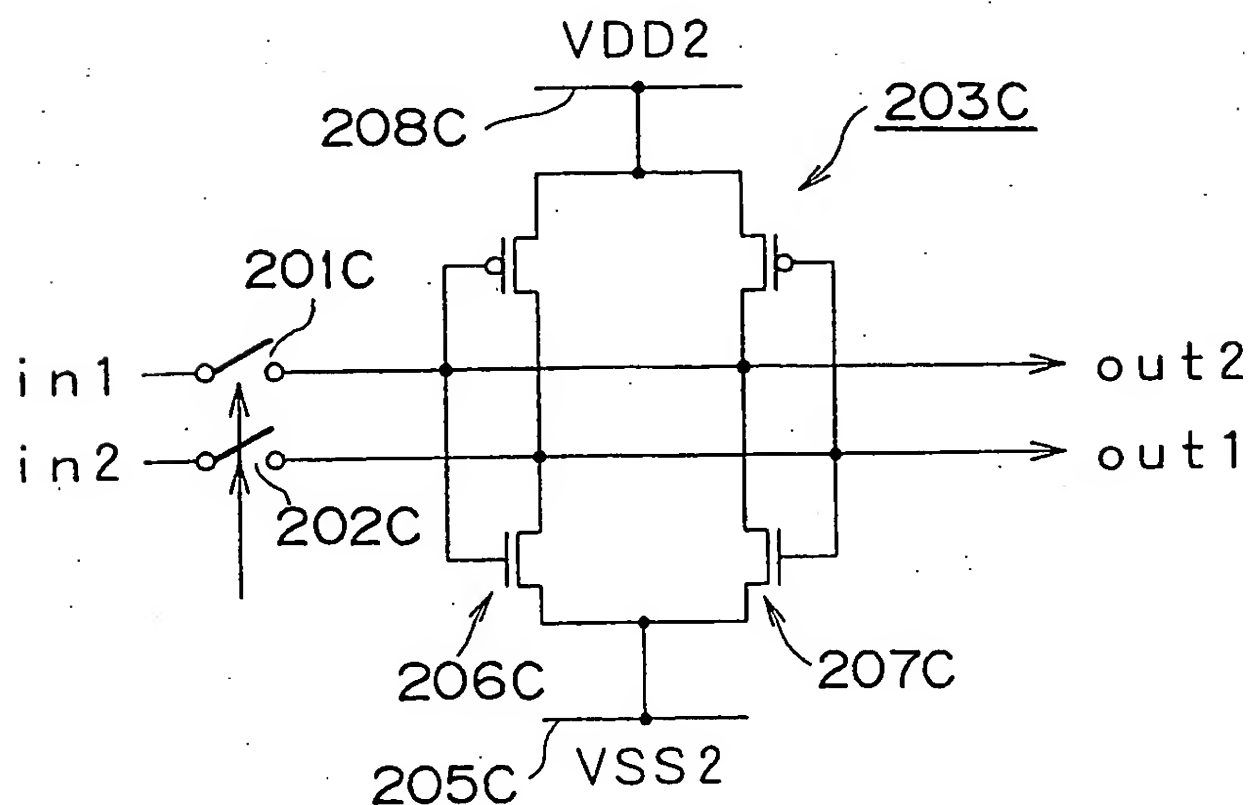


FIG. 14

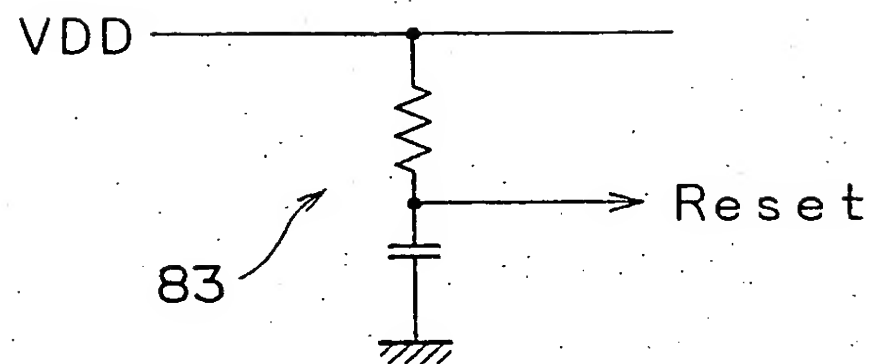


FIG. 14A
RELATED ART 2

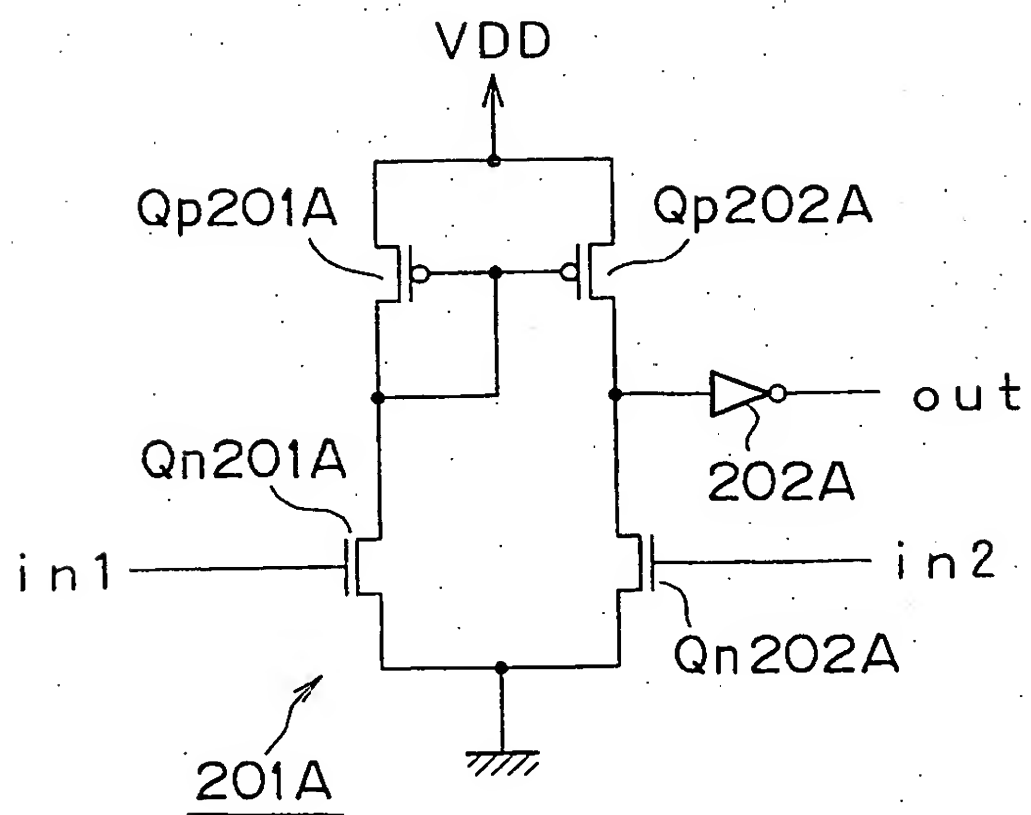


FIG. 15

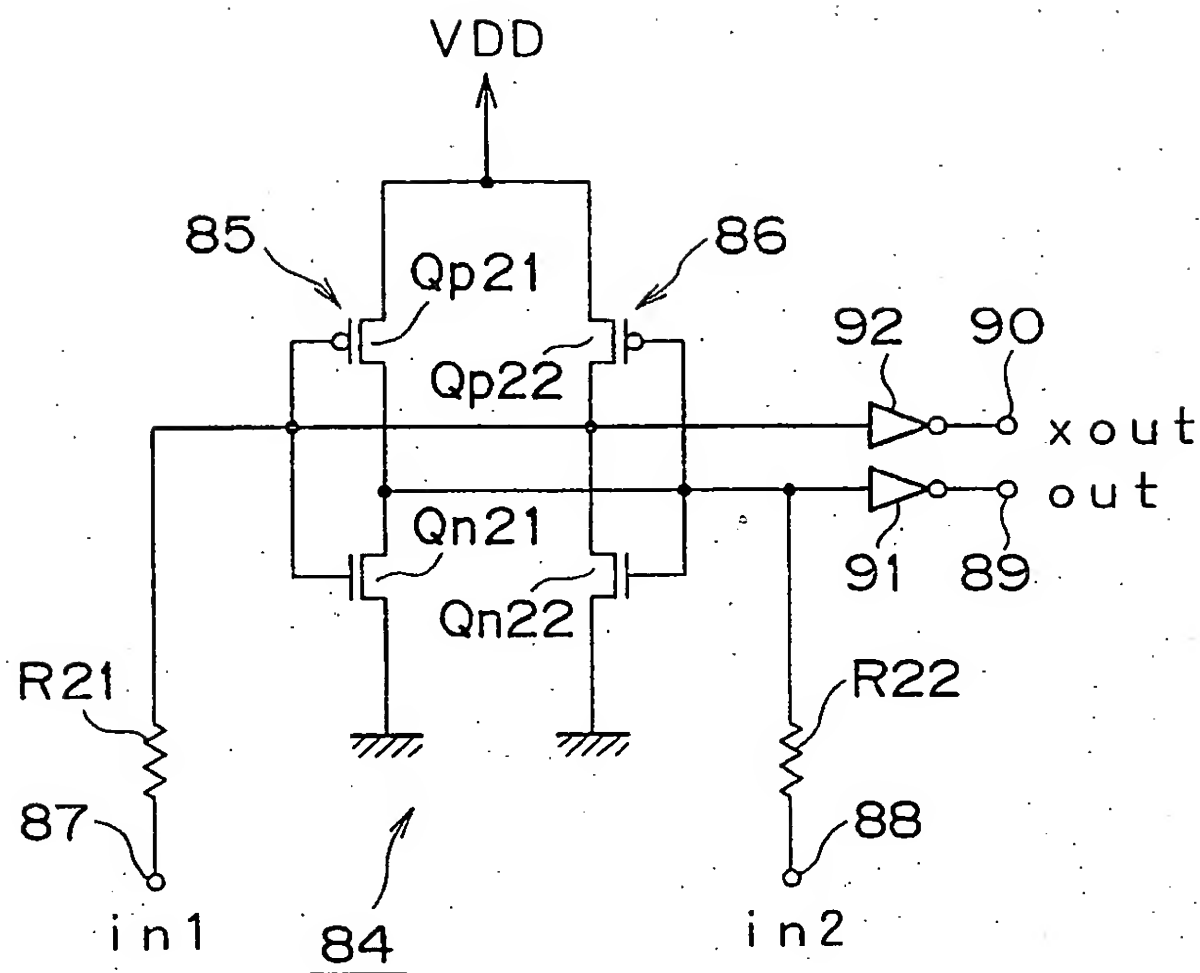


FIG. 15A RELATED ART 3

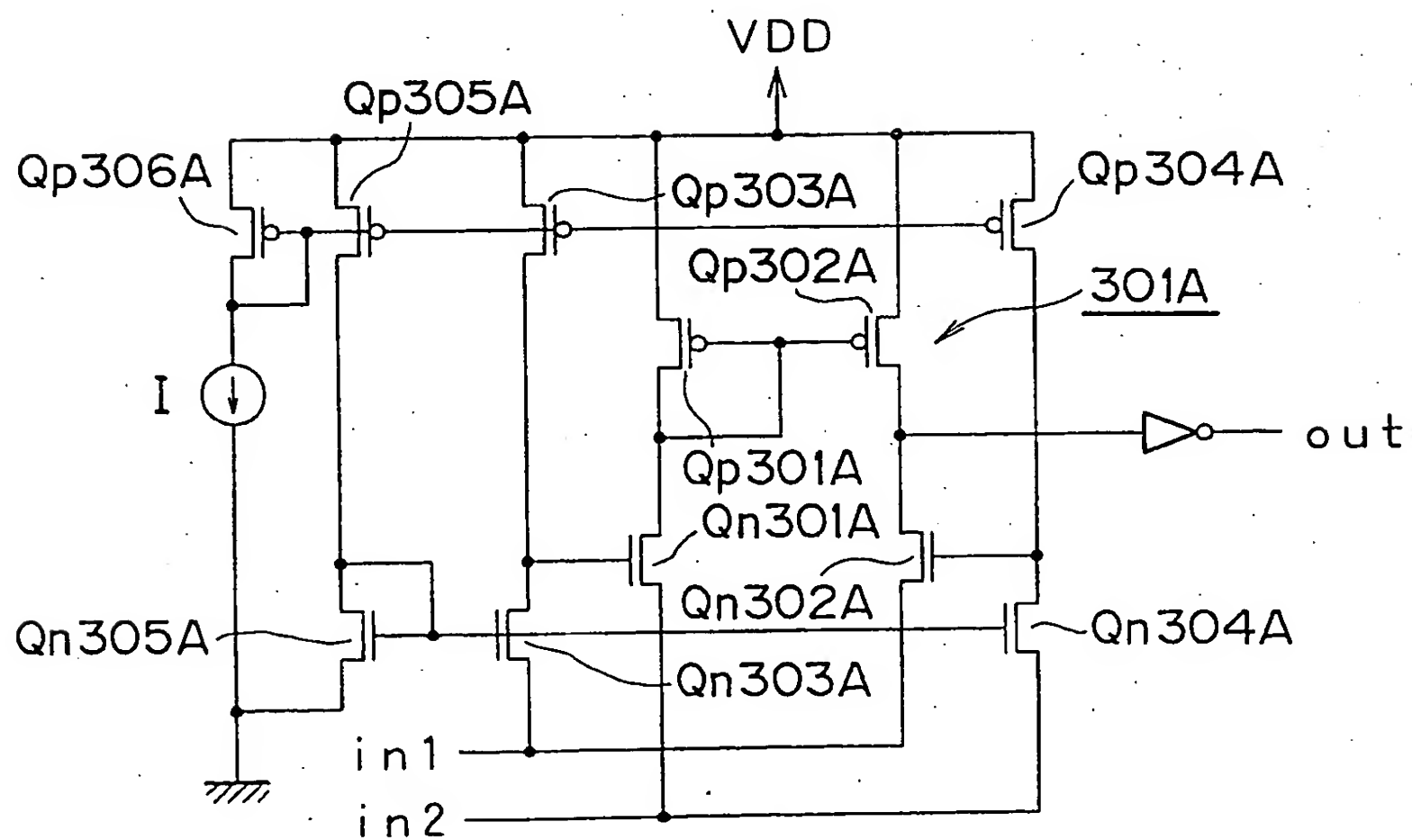
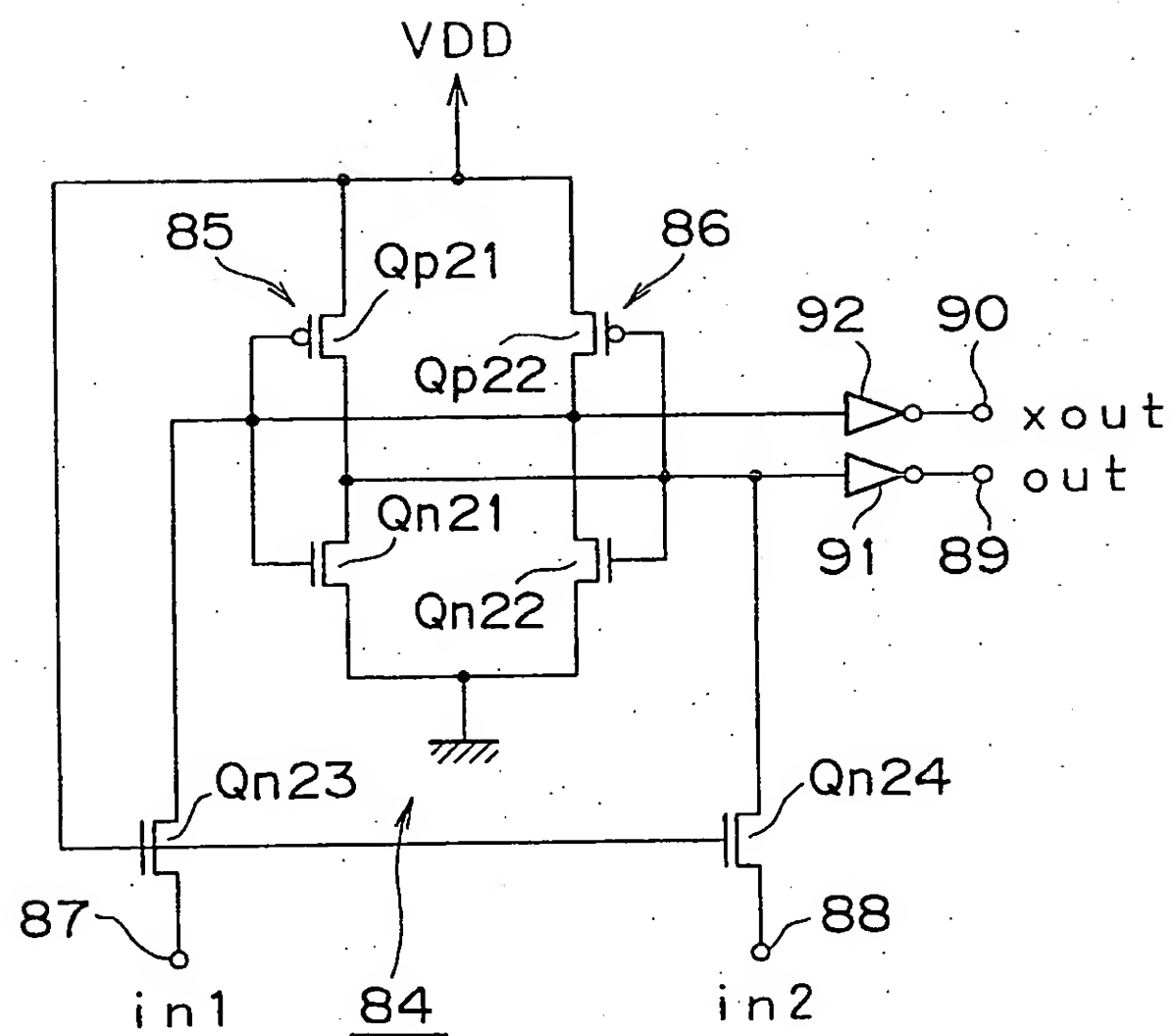


FIG. 16



F 1 G 17

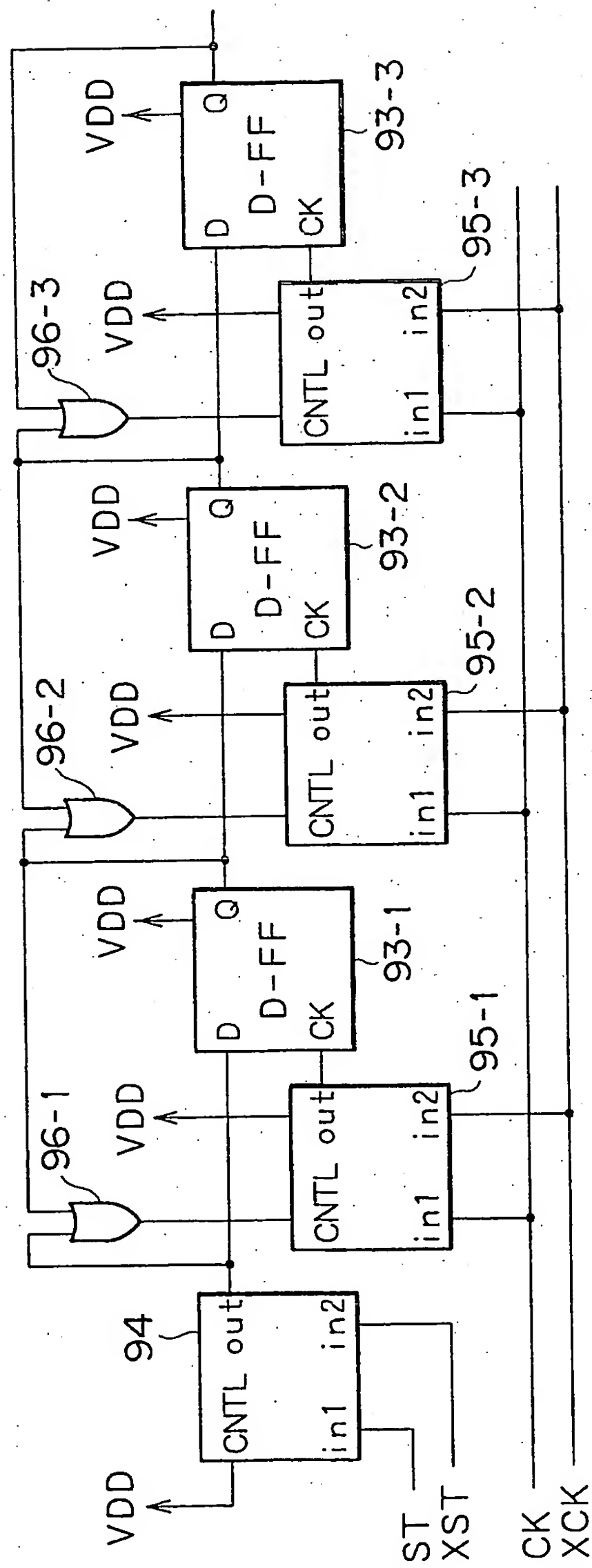


FIG. 18

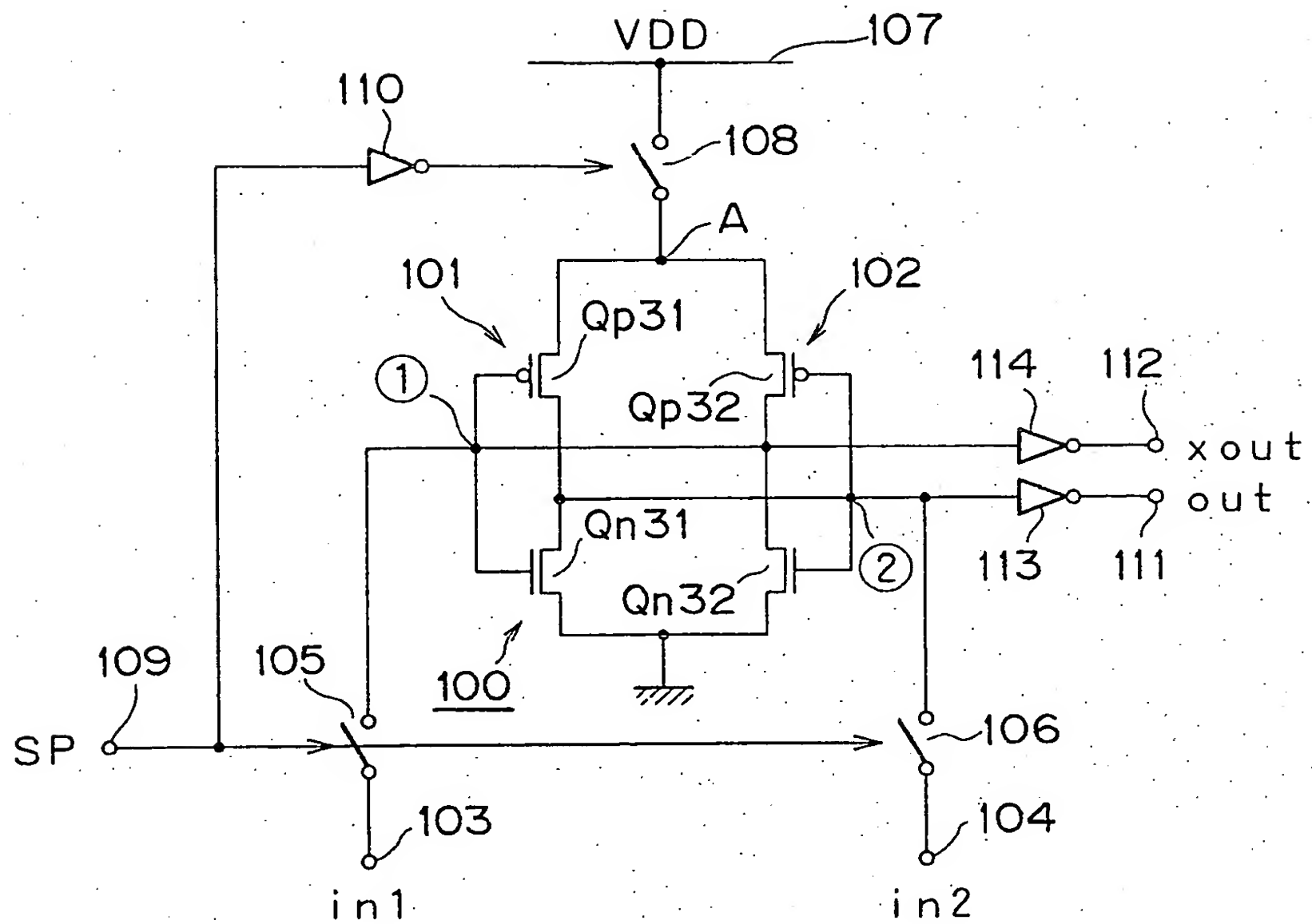


FIG. 19

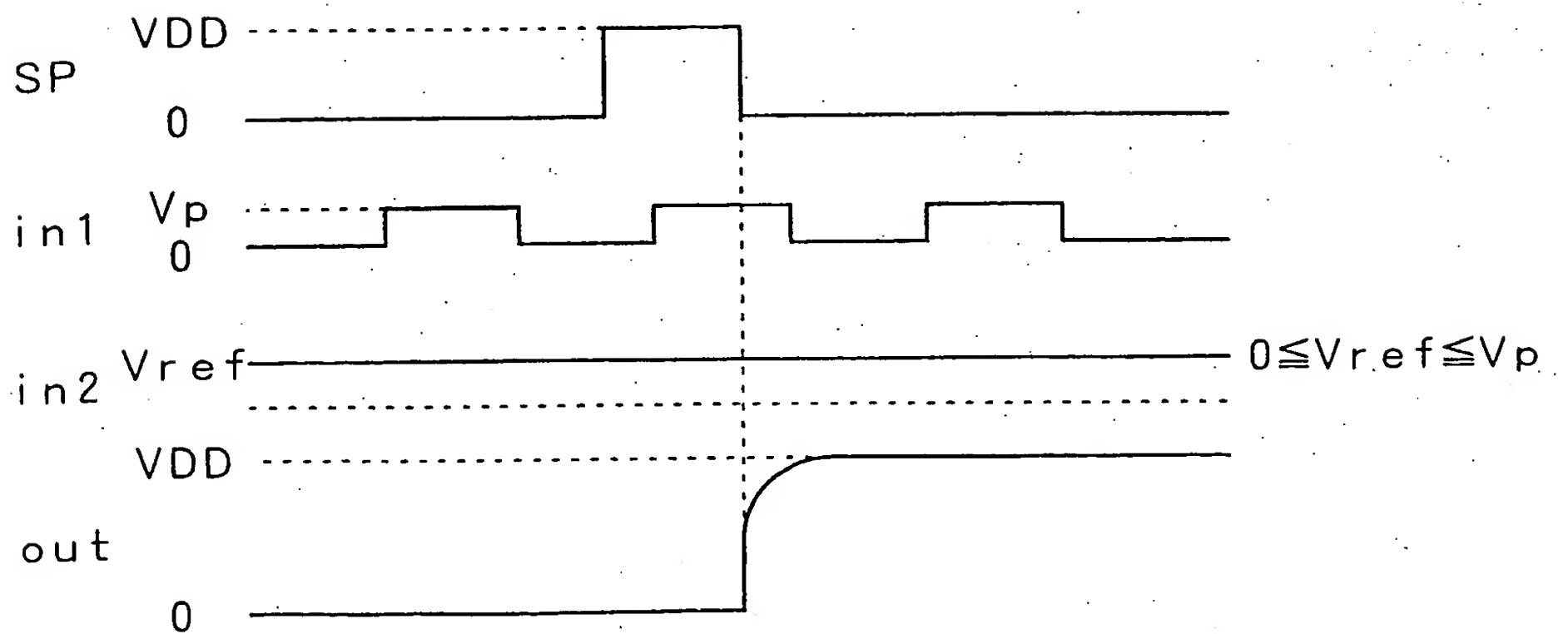


FIG. 20

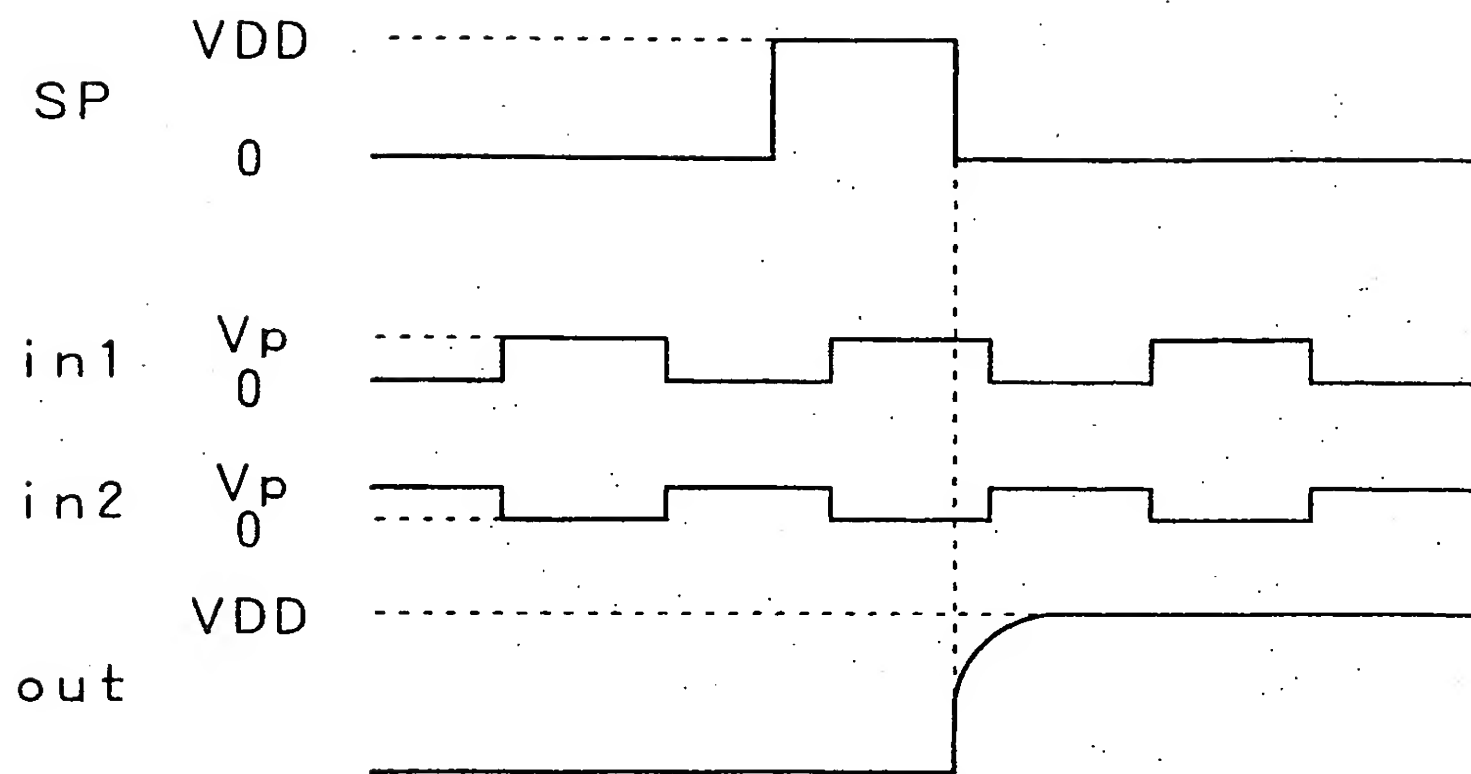


FIG. 21

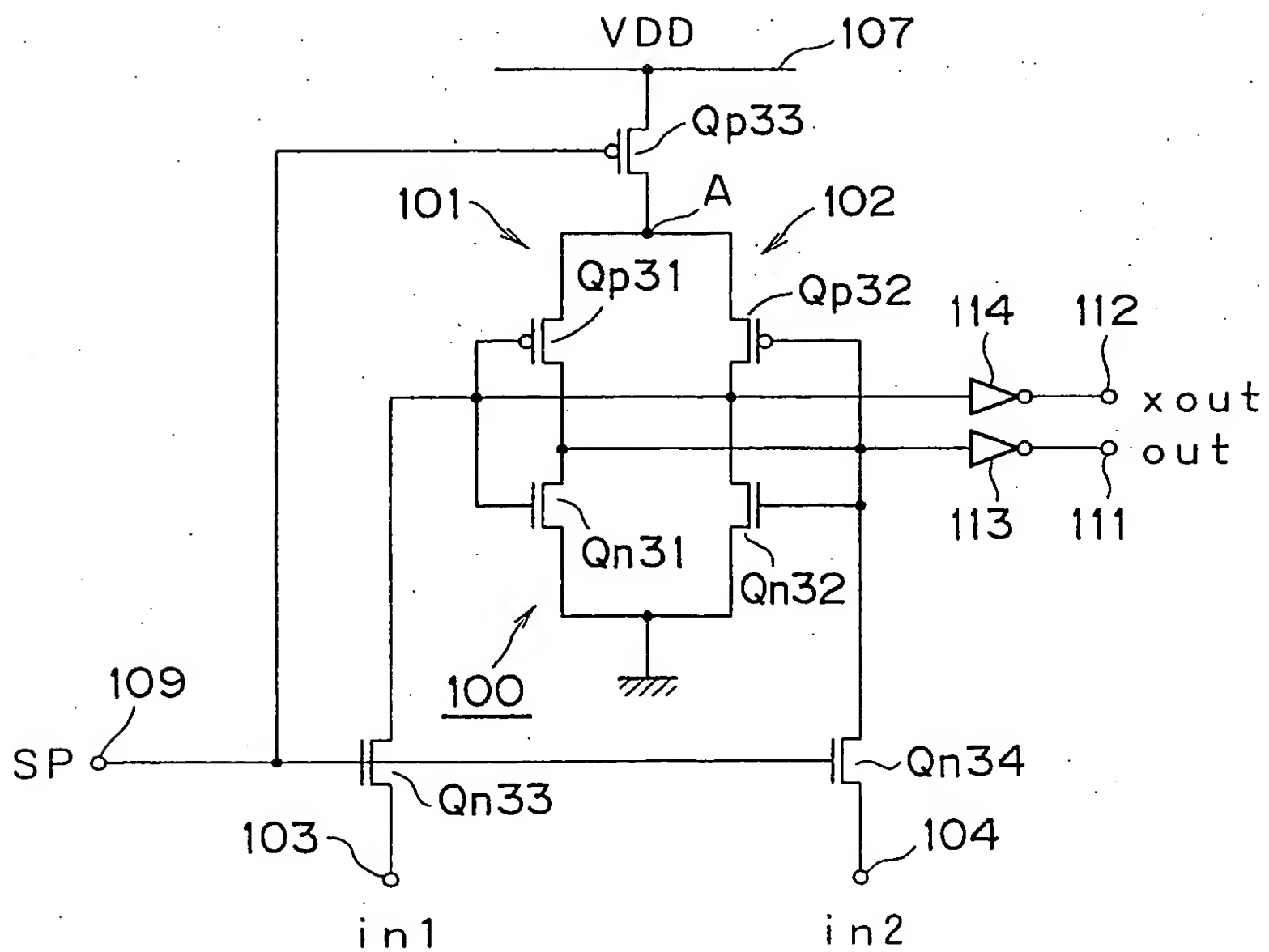


FIG. 22

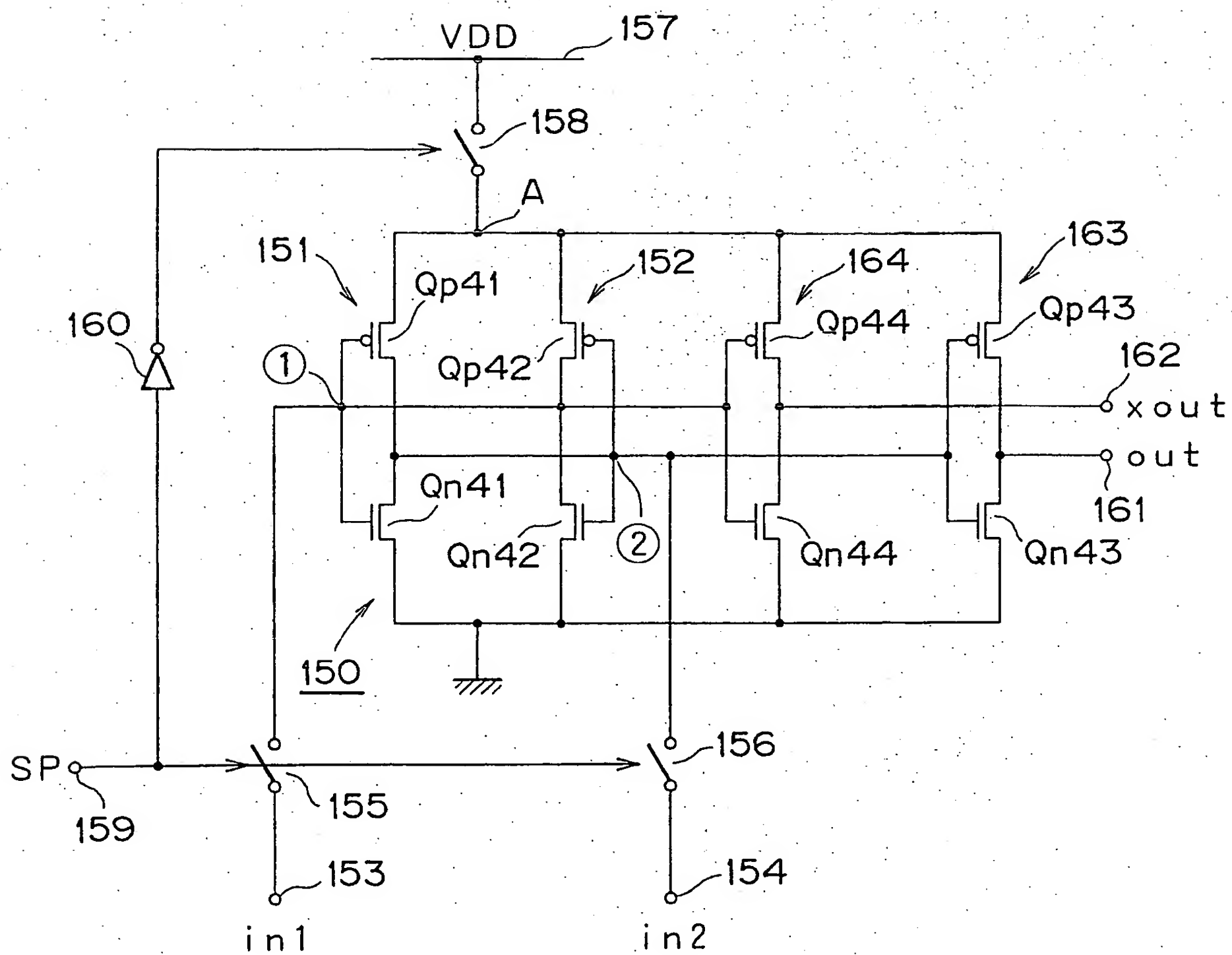


FIG. 23

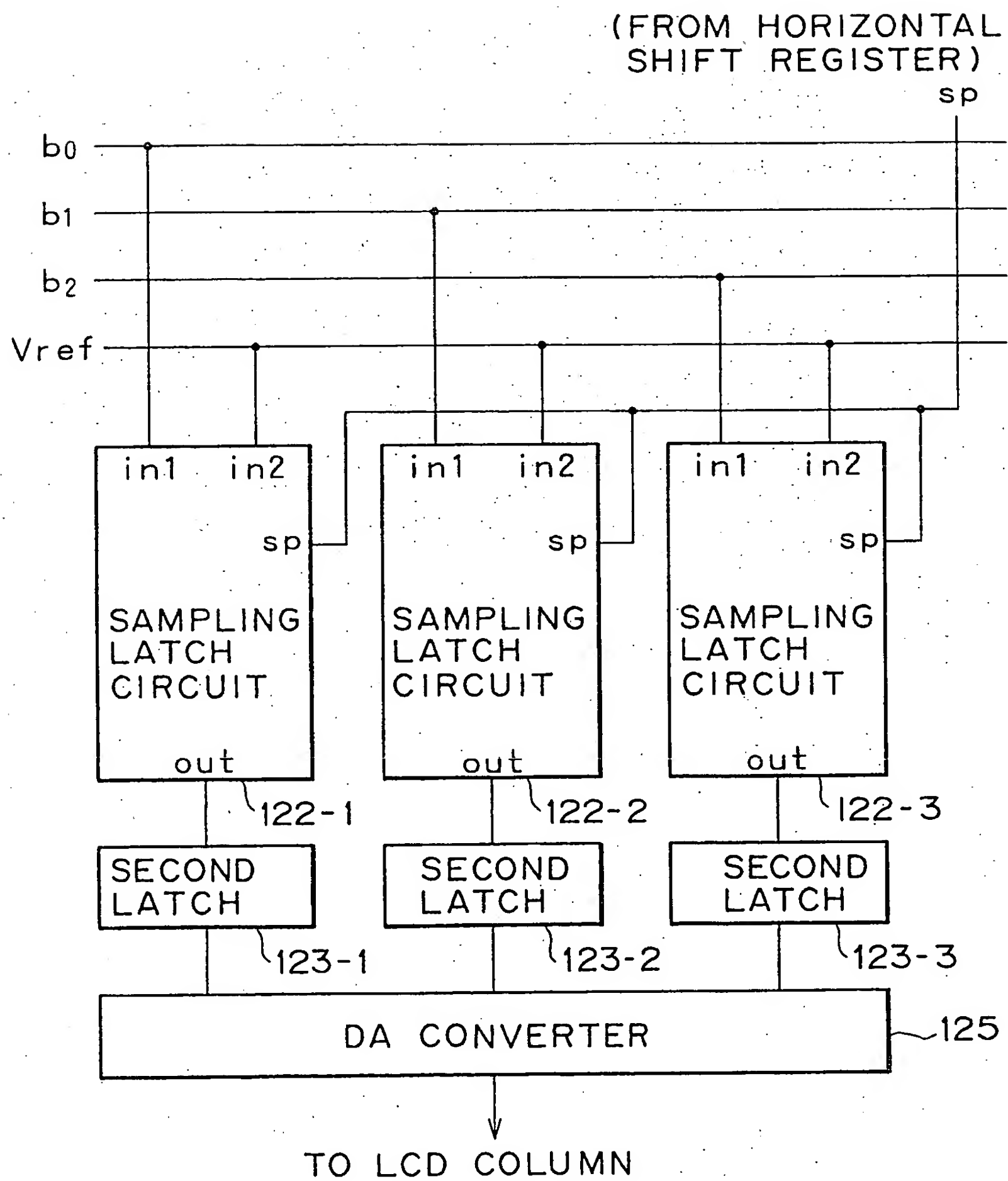


FIG. 24

(FROM HORIZONTAL
SHIFT REGISTER)

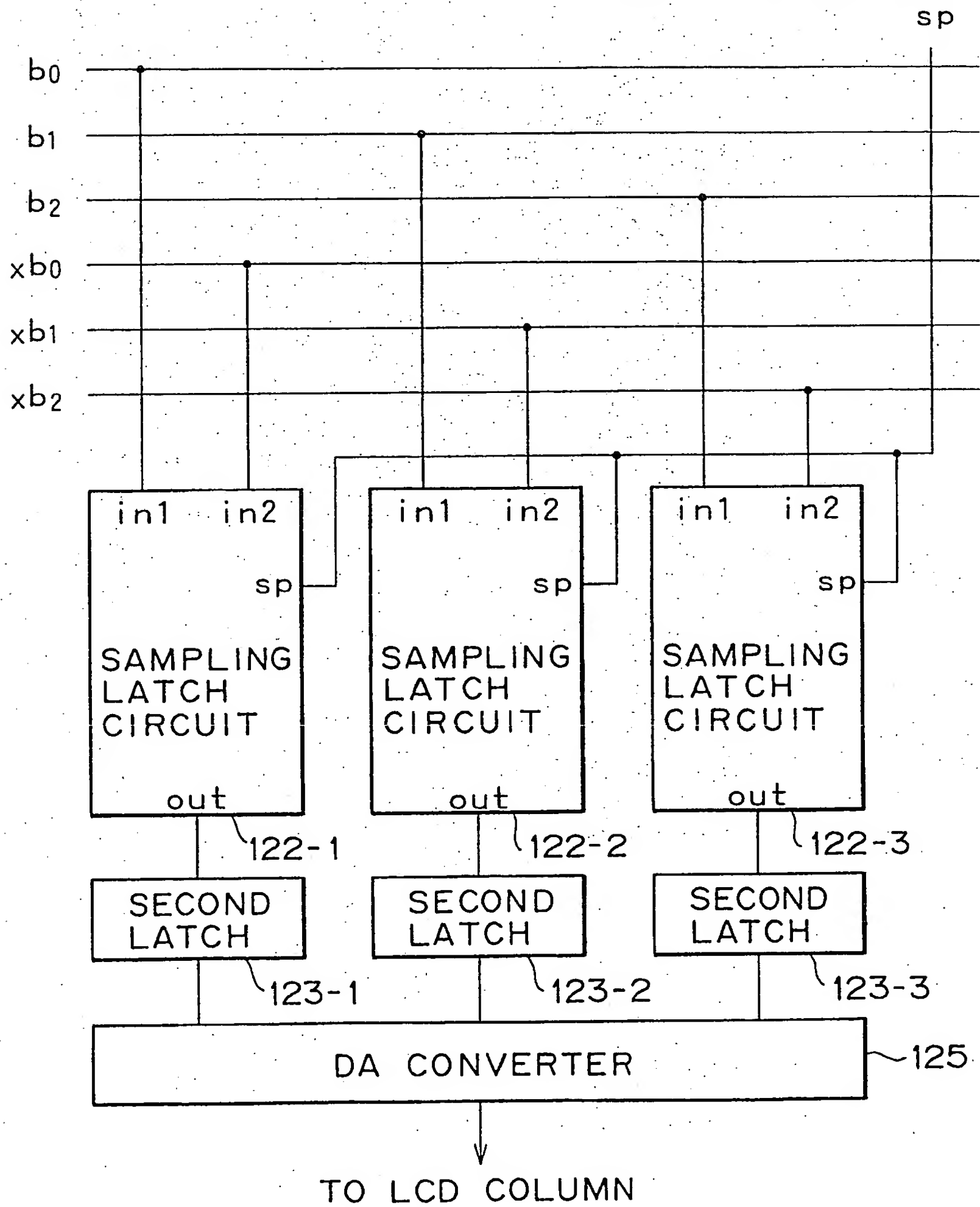


FIG. 25

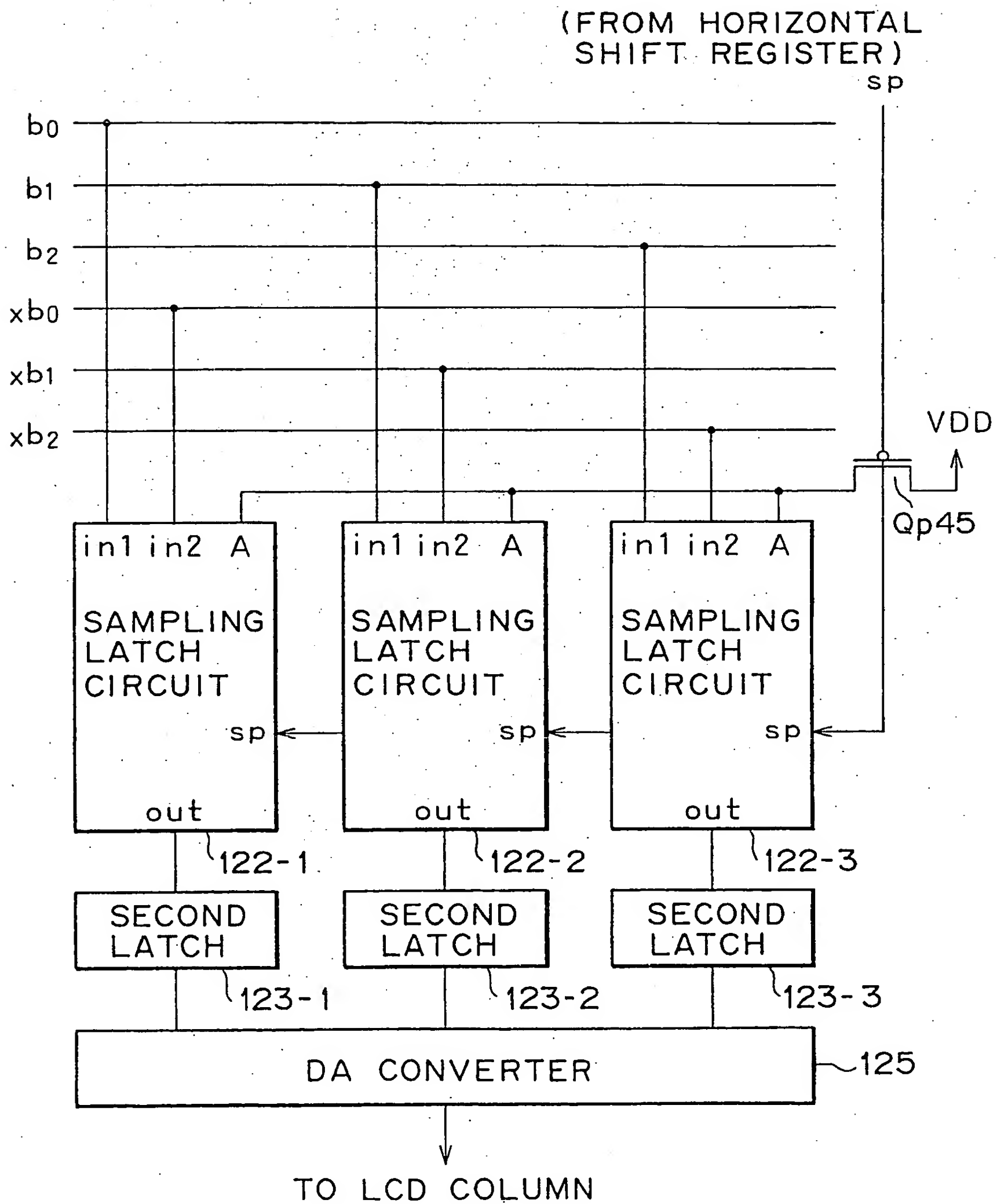


FIG. 26

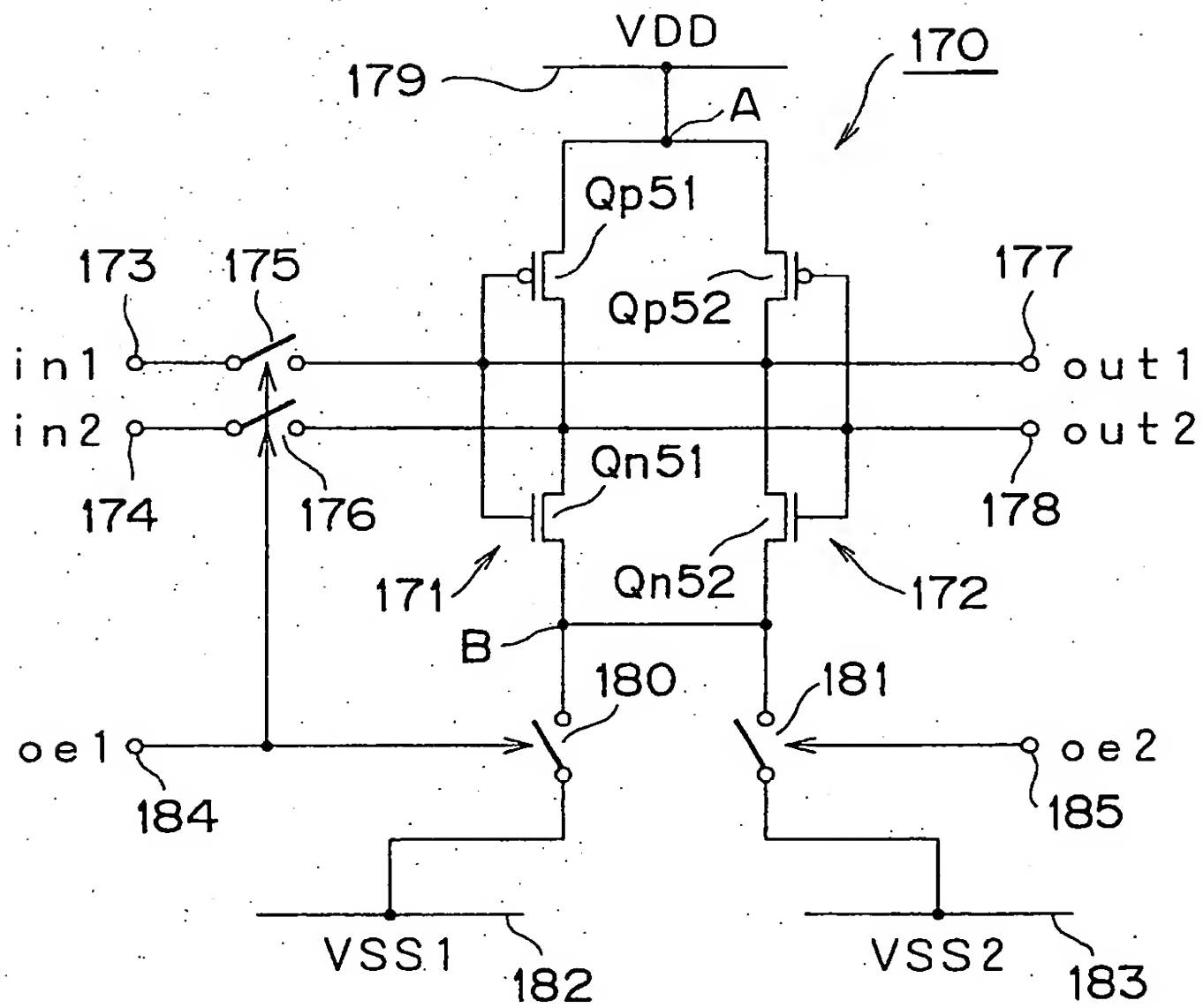


FIG. 27

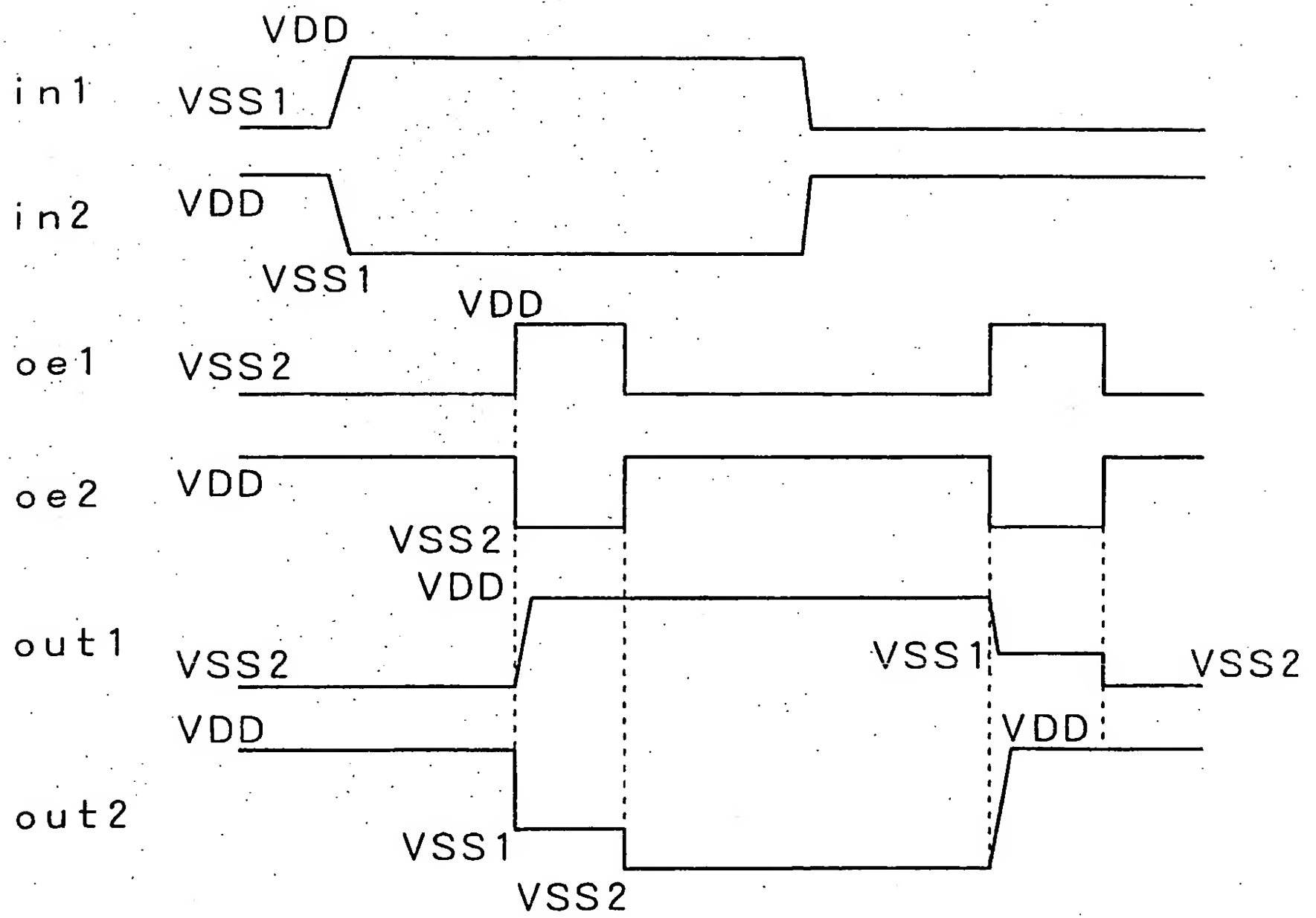


FIG. 28

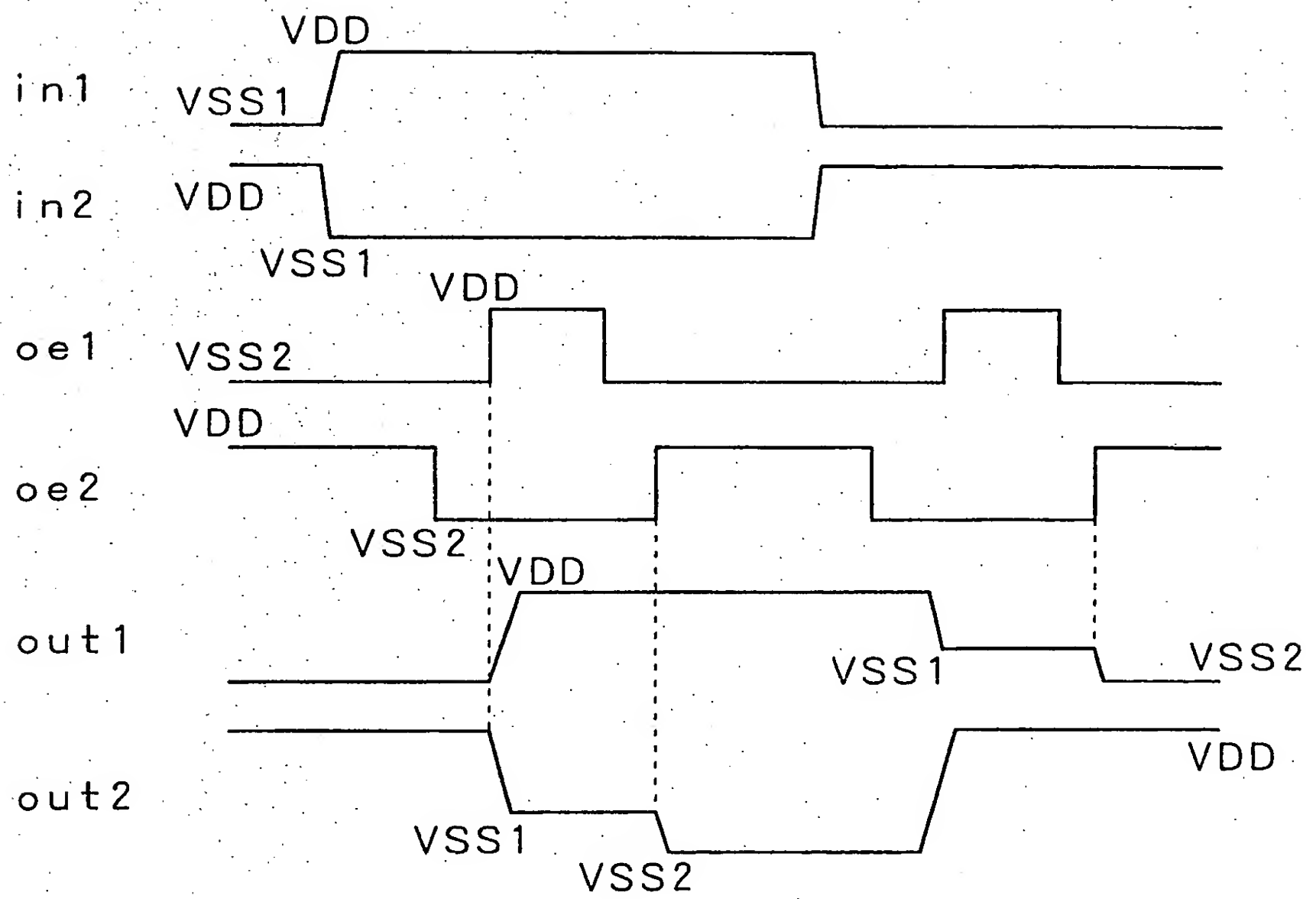


FIG. 29

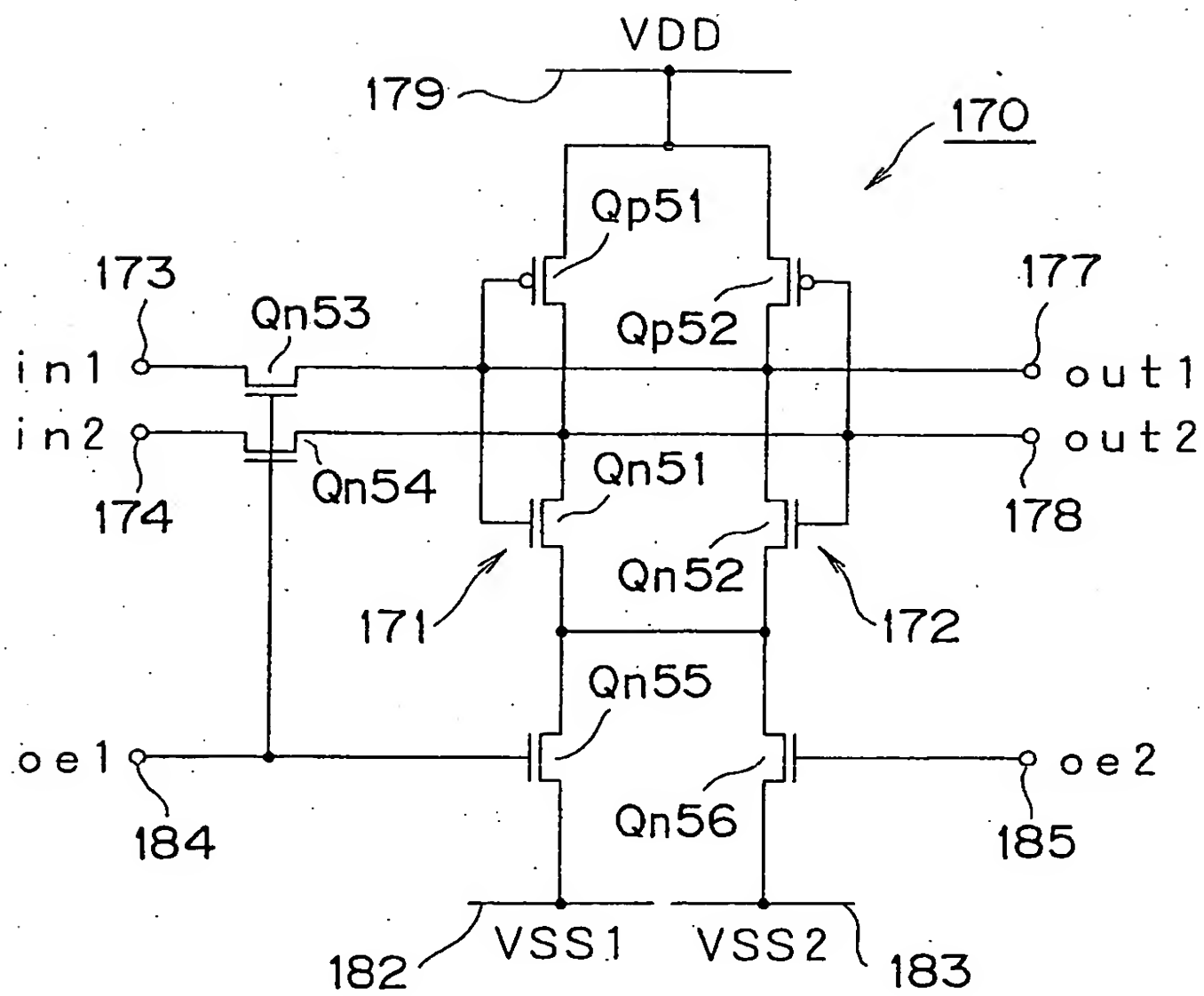


FIG. 30

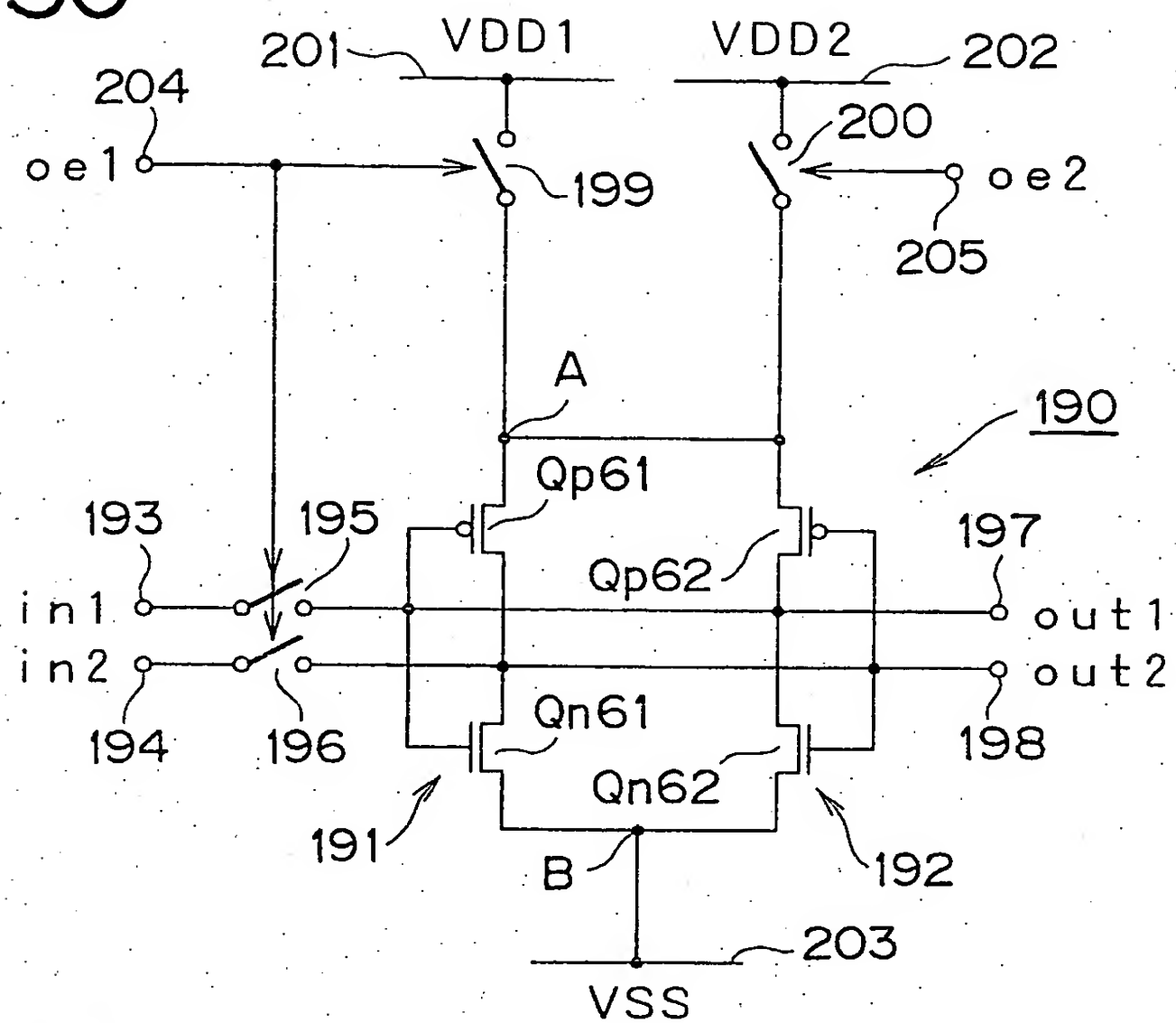


FIG. 31

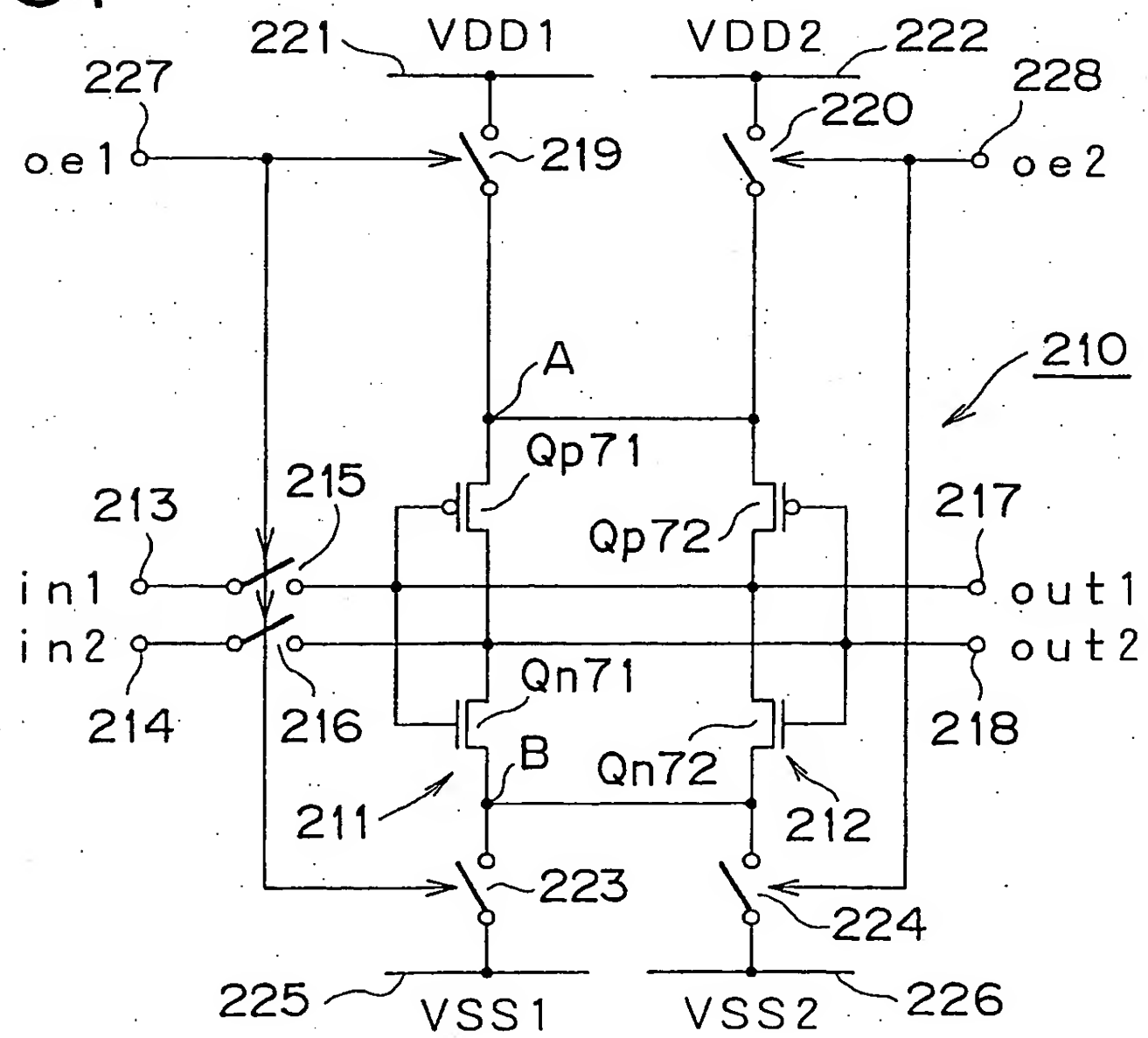


FIG. 32

SAMPLING PULSE
(FROM HORIZONTAL
SHIFT REGISTER)

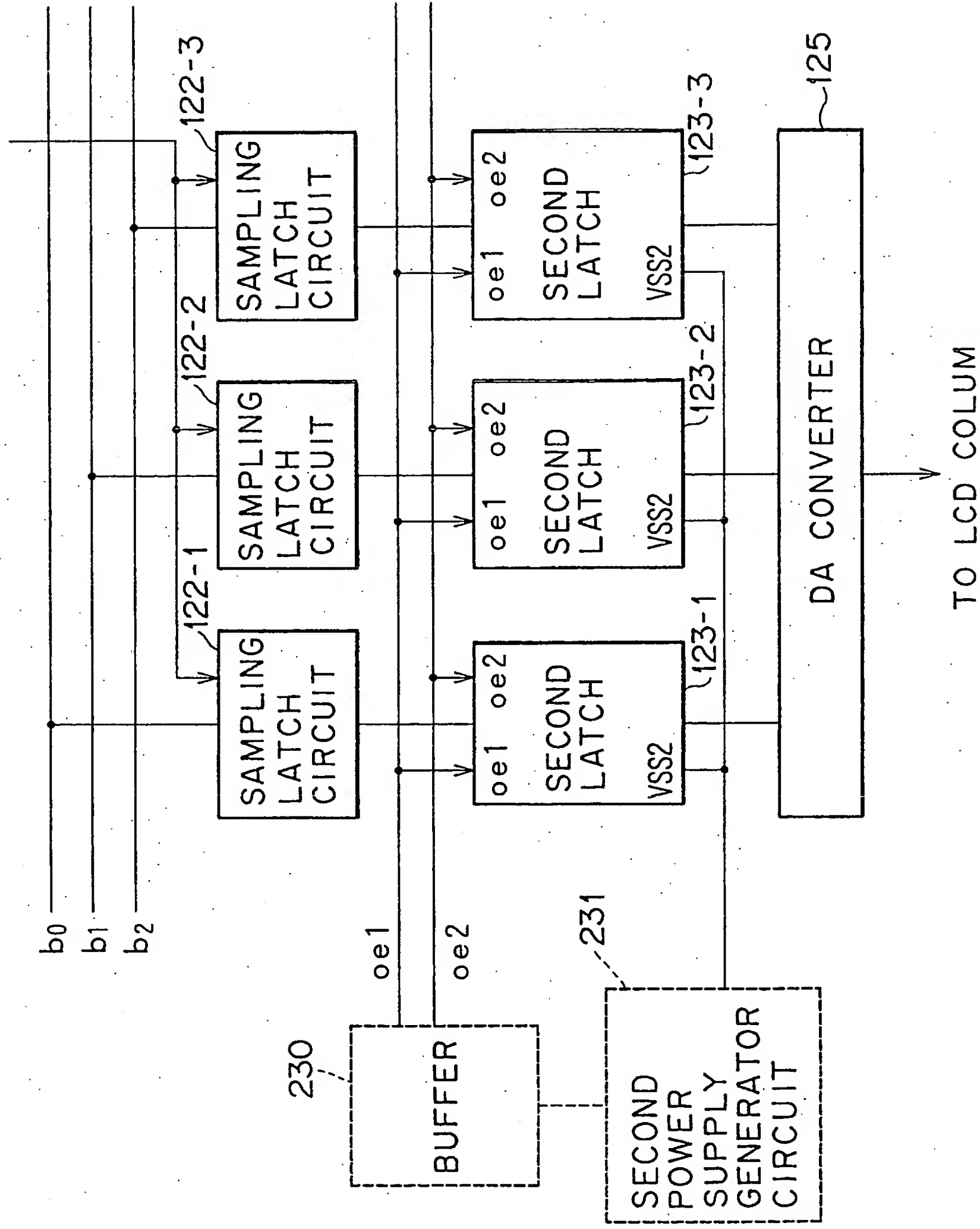


FIG. 33

SAMPLING PULSE
(FROM HORIZONTAL
SHIFT REGISTER)

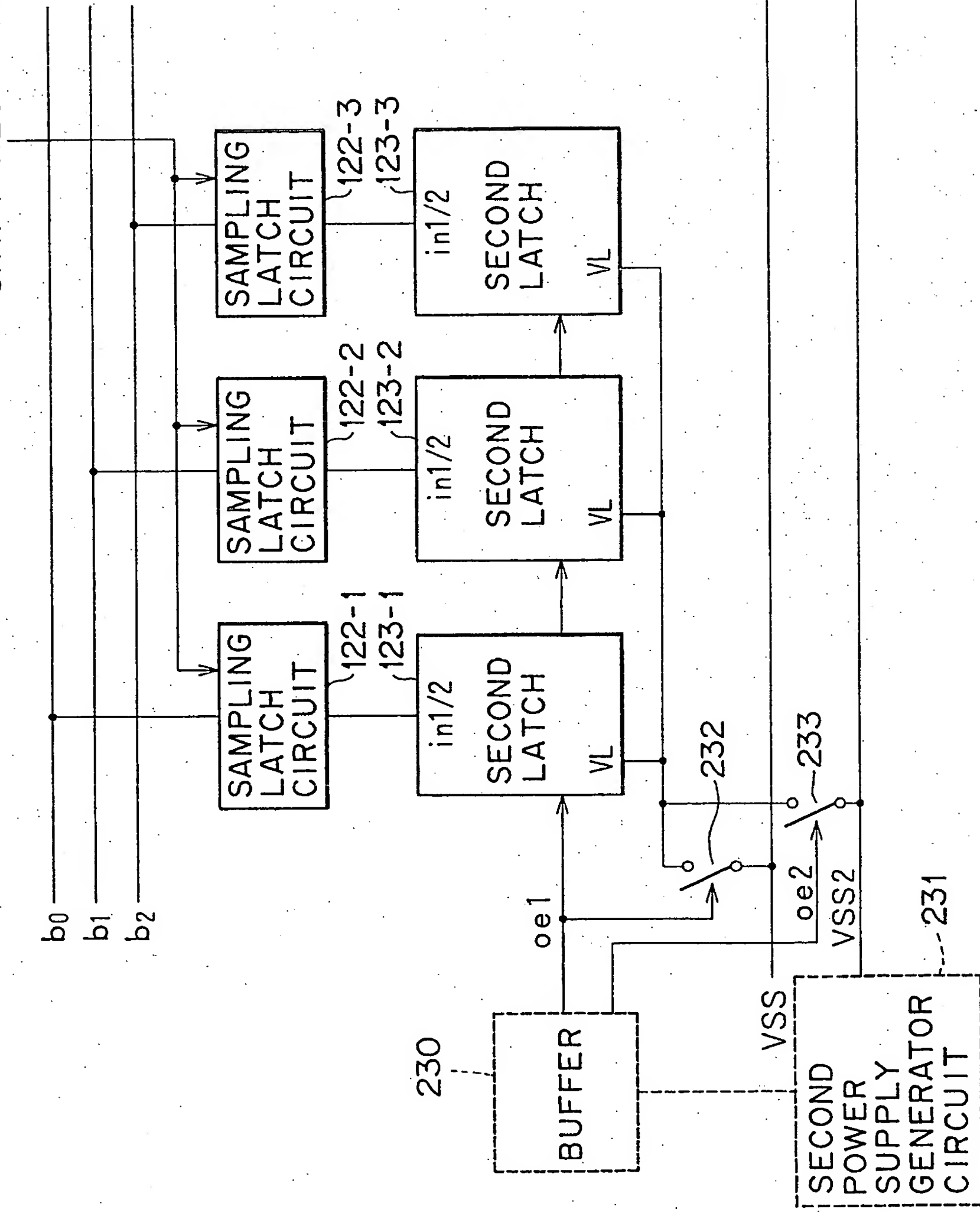


FIG. 34

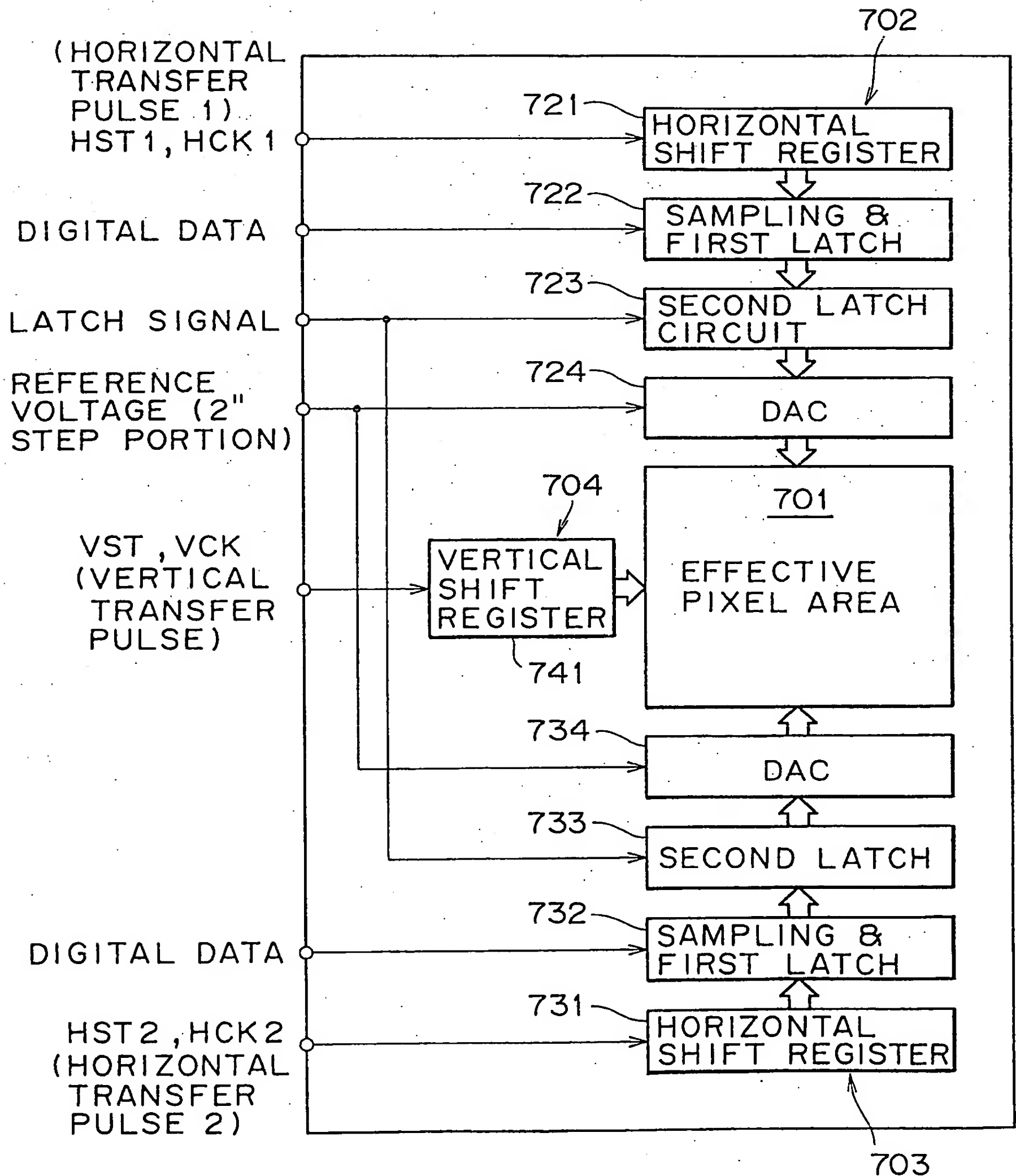


FIG. 35

